

Compal Confidential

QAWYA M/B Schematics Document

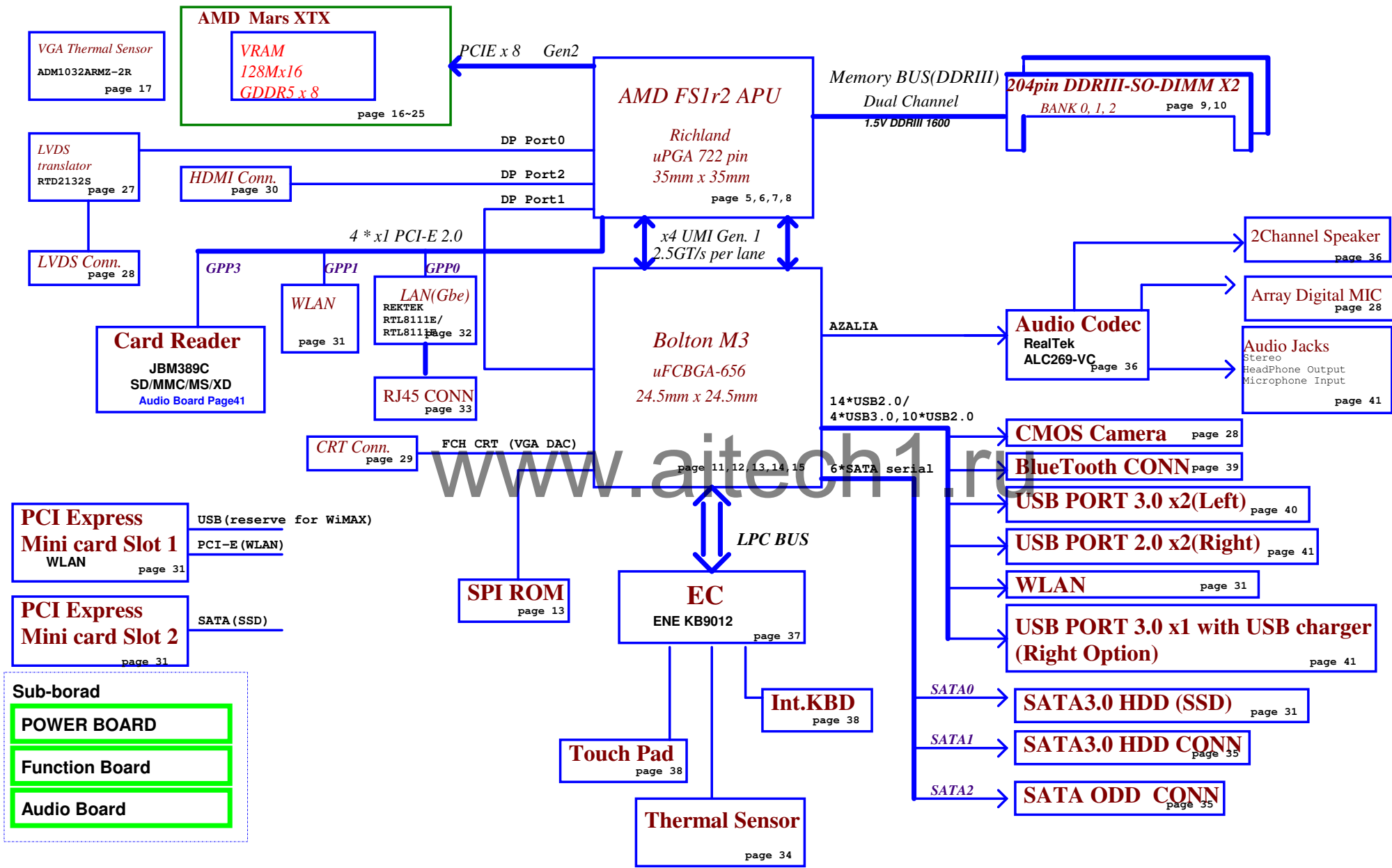
AMD Fs1r2 Processor with DDRIII + Bolton M3 FCH

AMD VGA Mars XTX

2012-11-01

REV : 0 . 1

| | | | | | |
|---|--------------------|-----------------|------------|--------------------------|-----------------------------|
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| Issued Date | 2012/07/03 | Deciphered Date | 2013/07/03 | Title | Cover Page |
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| | | | | Date | Thursday, November 01, 2012 |
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| power plane State | +B | +5VALW +3VALW +1.1VALW | +1.5V +1.5V_APU | +5VS +3VS +2.5VS +1.5VS +1.2VS +1.1VS +0.75VS +APU_CORE +APU_CORE_NB +VGA_CORE +3.3VGS +1.8VGS +1.5VGS +0.95VGS |
|--------------------------------|----|--------------------------------------|------------------------|--|
| S0 | O | O | O | O |
| S3 | O | O | O | X |
| S5 S4/AC | O | O | X | X |
| S5 S4/ Battery only | O | X | X | X |
| S5 S4/AC & Battery don't exist | X | X | X | X |

BOARD ID Table

Board ID / SKU ID Table for AD channel

Ra = R310
Rb = R311

BOM Structure Table

| BOM Structure | BTO Item |
|---------------|----------------------------|
| PX@ | VGA circuit |
| CHG@ | USB charger part |
| NOCHG@ | No USB charger part |
| BT@ | Blue Tooth part |
| CMOS@ | CMOS Camera part |
| 8111E@ | RTL8111E LAN part |
| 8111F@ | RTL8111F LAN part |
| LAN_E@ | RTL8111E X76 |
| LAN_F@ | RTL8111F X76 |
| X76@ | X76 Level part for VRAM |
| S1G@ | X76 P/N for Samsun VRAM 1G |
| S2G@ | X76 P/N for Samsun VRAM 2G |
| H1G@ | X76 P/N for Hynix VRAM 1G |
| H2G@ | X76 P/N for Hynix VRAM 2G |
| 1403@ | EMC1403 thermal part |
| 2103@ | EMC2103 thermal part |
| HDMI@ | HDMI part |
| KBL@ | K/B Light part |
| ME@ | ME part |
| USBR3@ | Right port 3.0 |
| USBR2@ | Right port 2.0 |
| SSD@ | SSD |
| @ | Unpop |
| 45@ | HDMI Logo |
| | |
| | |
| | |
| | |
| | |

| OC# | USB Port | |
|-----|---------------------|-------------------|
| 0 | USB20 port10,port11 | USB30 port0,port1 |
| 1 | USB20 port0 | |
| 2 | USB20 port1,port12 | USB30 port2 |
| 3 | | |

| Port | Device |
|------|-------------|
| 1 | LAN |
| 2 | WLAN |
| 3 | |
| 4 | Card Reader |

| Port | Device |
|------|--------|
| 1 | |
| 2 | |
| 3 | |
| 4 | |

| | SOURCE | VGA | BATT | KB9012 | SODIMM | WLAN WWAN | Thermal Sensor | FCH | APU | RTD2132 |
|----------------------------------|--------------------------------|-----|-------------|--------|-----------|--------------|-------------------|-----|------------|---------|
| SMB_EC_CK1 SMB_EC_DA1 | KB9012 +3VALW | X | V +3VALW | X | X | X | X | X | X | X |
| SMB_EC_CK2_SUS SMB_EC_DA2_SUS | KB9012 +3VALW | X | X | X | X | X | X | X | V +1.5V | X |
| FCH_SCLK0 FCH_SDATA0 | FCH +3VS | X | X | X | V +3VS | V +3VS | X | X | X | X |
| SMB_EC_CK2 SMB_EC_DA2 | KB9012 +3VS (LV shifter) | V | X | X | X | X | V | X | X | V |

EC SM Bus2 address

| Device | Address | Device | Address |
|---------------|-------------|-------------------|------------|
| Smart Battery | 0001_011X b | Thermal Sensor | 1001_101xb |
| | | SB-TSI(default) | 1001_100xb |
| | | VGA(int. thermal) | 1000_001xb |
| | | RTD2132S | 1010_1000b |
| | | VGA(ext. thermal) | 0100_1101b |

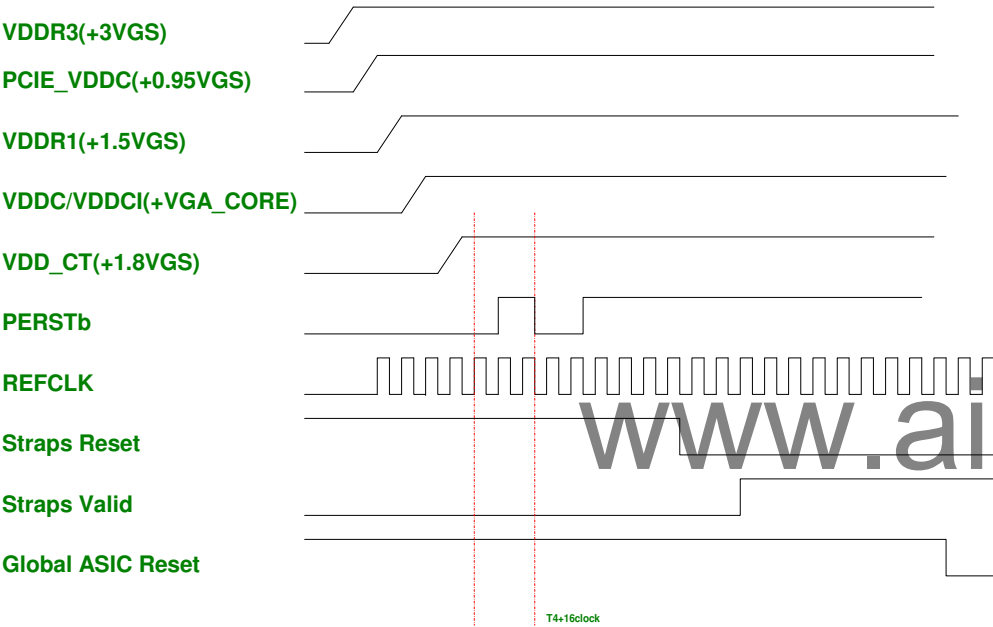
| Device | Address |
|-----------|------------|
| DDR DIMM0 | 1001 000Xb |
| DDR DIMM2 | 1001 010Xb |

| | | | | | | |
|---|--------------------|-----------------|------------|--------------------------|-----------------------------|---------------|
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Power-Up/Down Sequence

"Mars" has the following requirements with regards to power-supply sequencing to avoid damaging the ASIC:

- All the ASIC supplies must reach their respective nominal voltages within 20ms of the start of the ramp-up sequence, though a shorter ramp-up duration is preferred. The maximum slew rate on all rails is 50mV/us.
- The external pull ups on the DDC/AUX signals (if applicable) should ramp up before or after both VDDC and VDD_CT have ramped up.
- VDDC and VDD_CT should not ramp up simultaneously. For example, VDDC should reach 90% before VDD_CT starts to ramp up (or vice versa).
- For power down, reversing the ramp-up sequence is recommended.

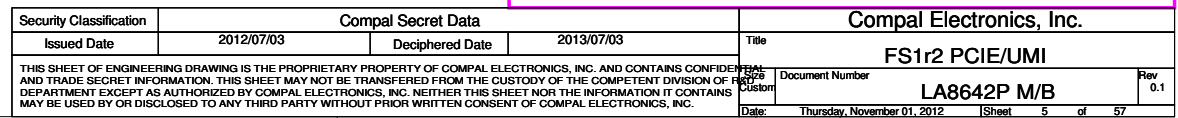


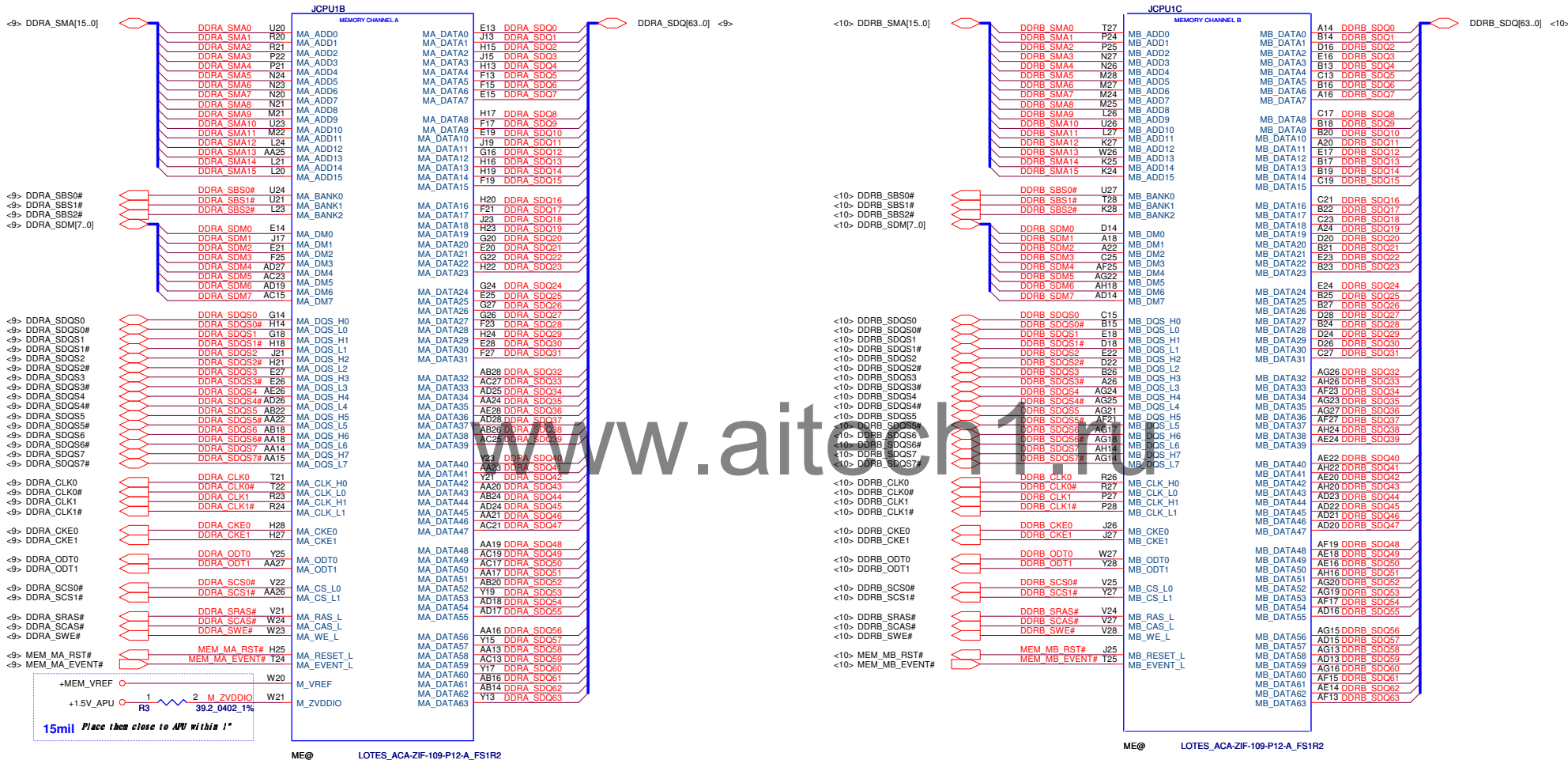
Mars XTX VRAM STRAP

| | | X76@ Vendor | | | | X76@ | |
|--------------|-----|--|---------|---------|---------|-------------|-------------|
| | | UV3,UV4,UV5,UV6 UV7,UV8,UV9,UV10 | PS_3[3] | PS_3[2] | PS_3[1] | R_pu R24 | R_pd R26 |
| ZZZ2 H2G@ | 2GB | SK Hynix Gemma 2Gb SA00004GD40/SA00004GD60 128Mx16 H5GQ2H24AFR-T2C | 0 | 0 | 0 | NC | 4.75K |
| ZZZ3 S2G@ | 2GB | Samsung D-die 2Gb SA00005B700/SA00005B710 128Mx16 K4G20325FD-FC04 | 0 | 0 | 1 | 8.45K | 2K |
| | | | 0 | 1 | 0 | 4.53K | 2K |
| | | | 0 | 1 | 1 | 6.98K | 4.99K |
| | | | 1 | 1 | 0 | 3.4K | 10K |

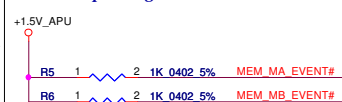


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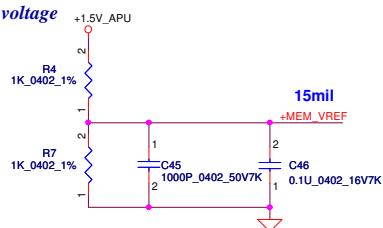




EVENT# pull high



0.75V reference voltage



| | | | | | |
|---|------------|--------------------|------------|--------------------------|-------------------------|
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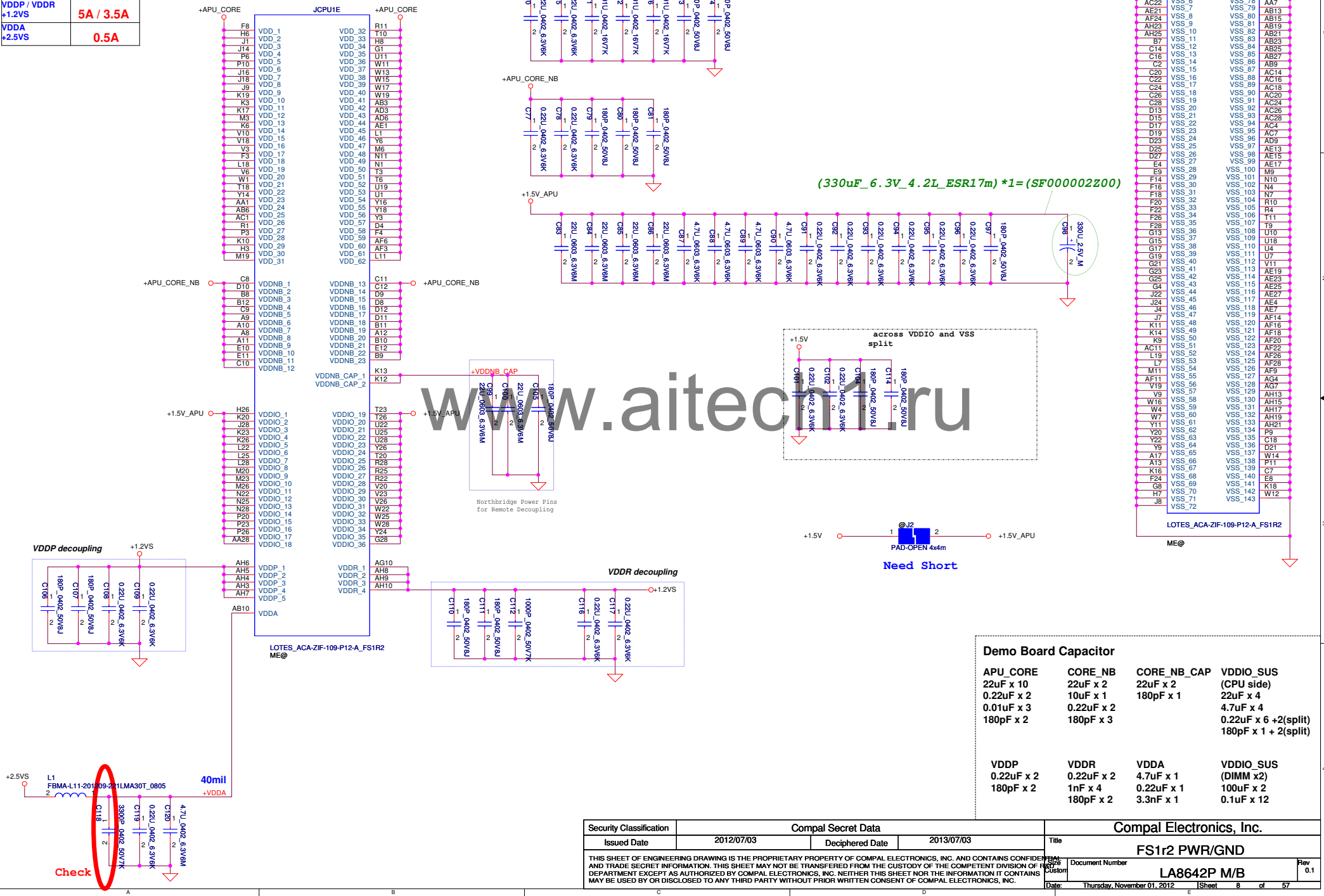
Compal Electronics, Inc.

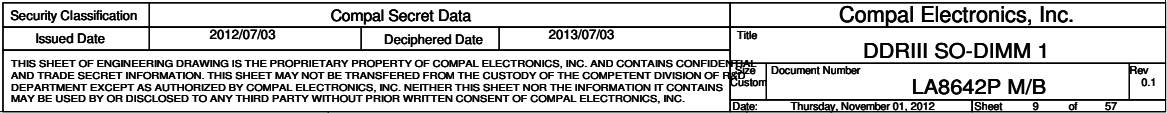
FS1r2 DDRIII Memory I/F

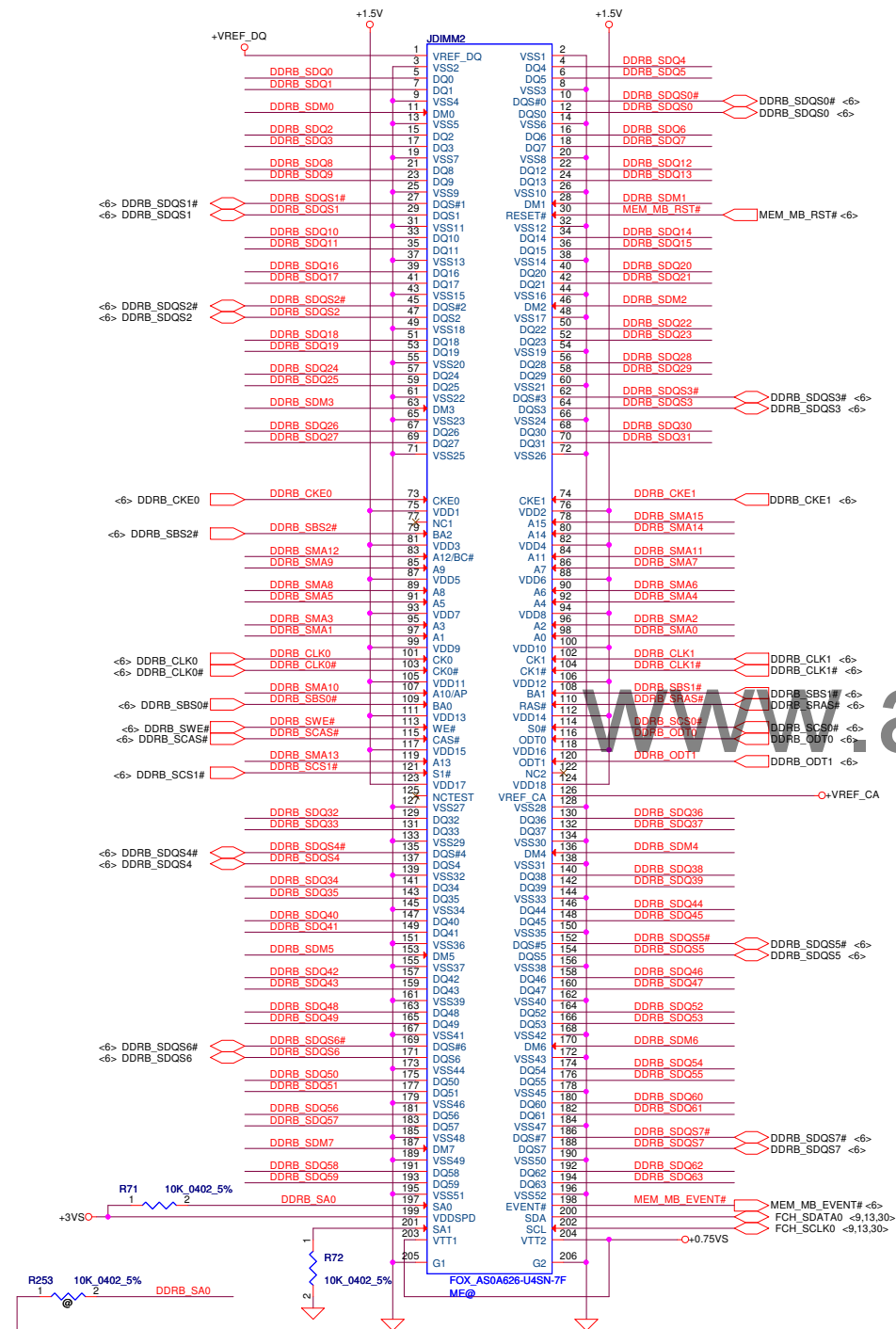
LA8642P M/B

Rev 0.1

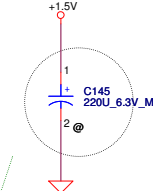
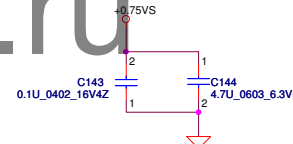
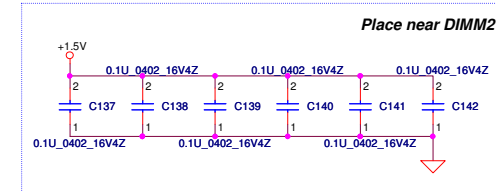
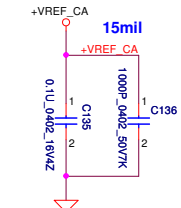
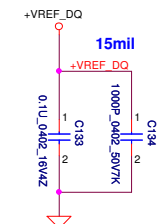
| Power Name | Consumption |
|-----------------------|-------------|
| VDD +APU_CORE | 60A |
| VDDNB +APU_CORE_NB | 29A |
| VDDIO +1.5V | 3.2A |
| VDDP / VDDR +1.2VS | 5A / 3.5A |
| VDDA +2.5VS | 0.5A |





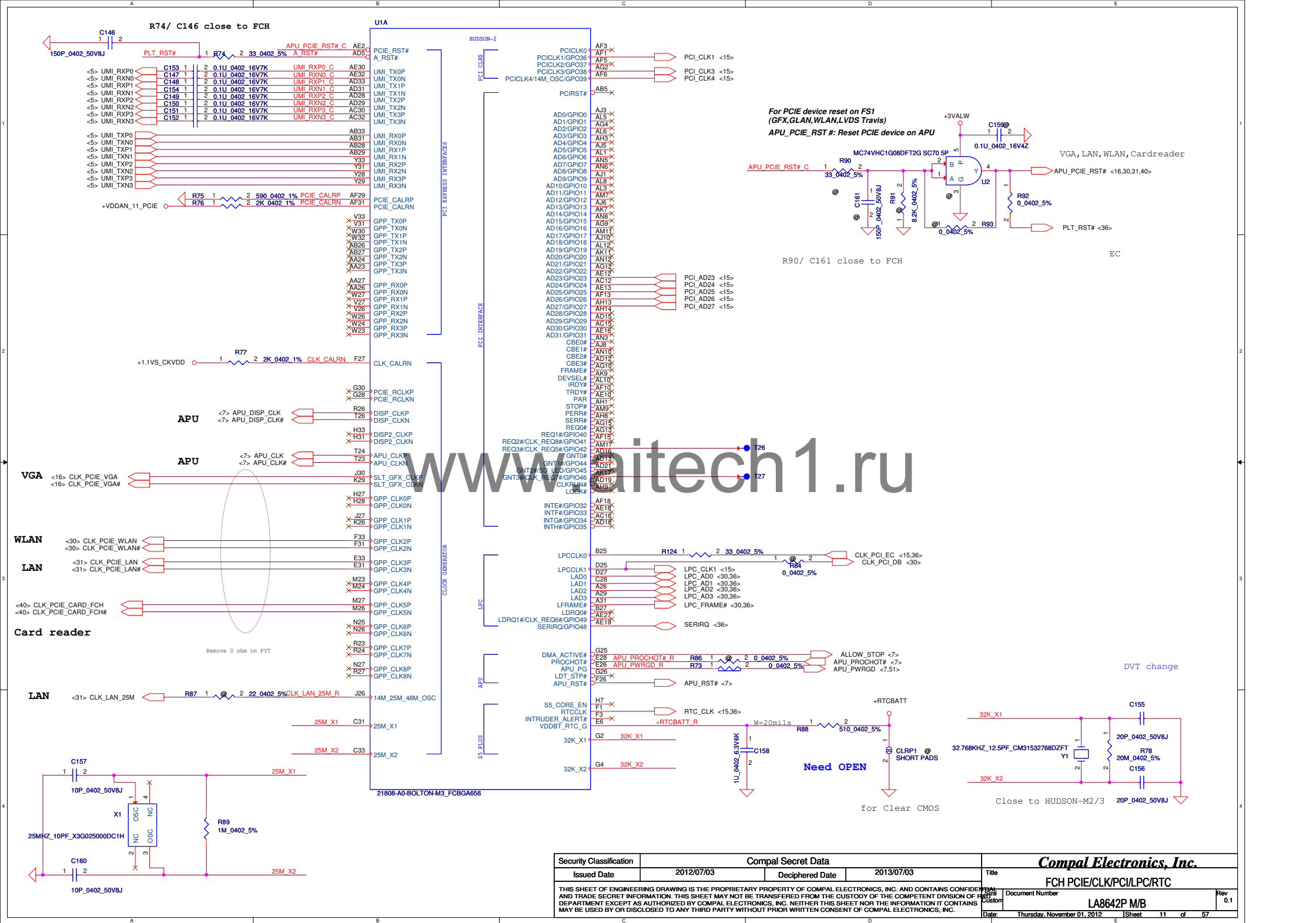


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 DDR_B_SDM[0..7] <6>
 DDR_B_SMA[0..15] <6>

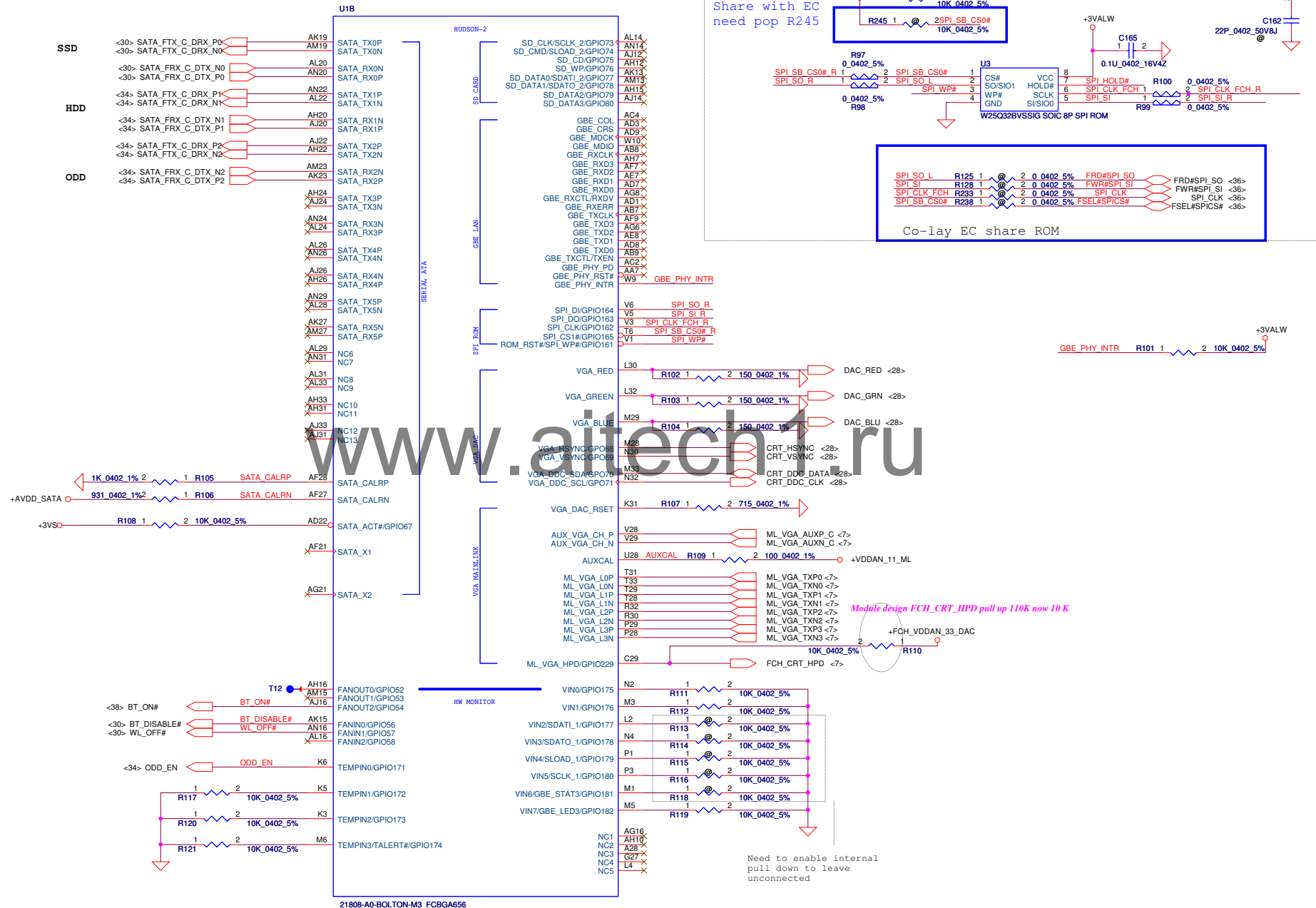


Standard H:4mm
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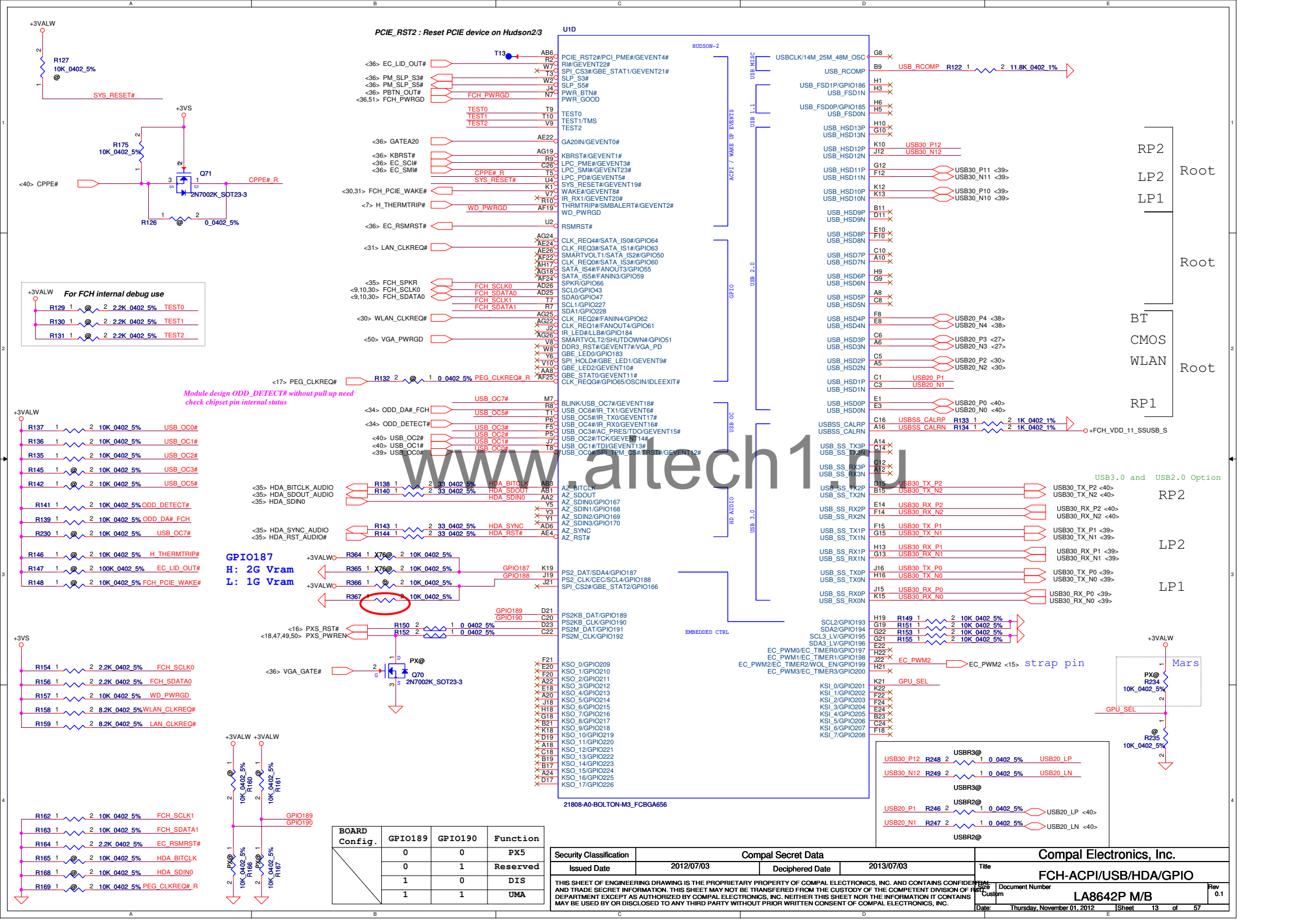
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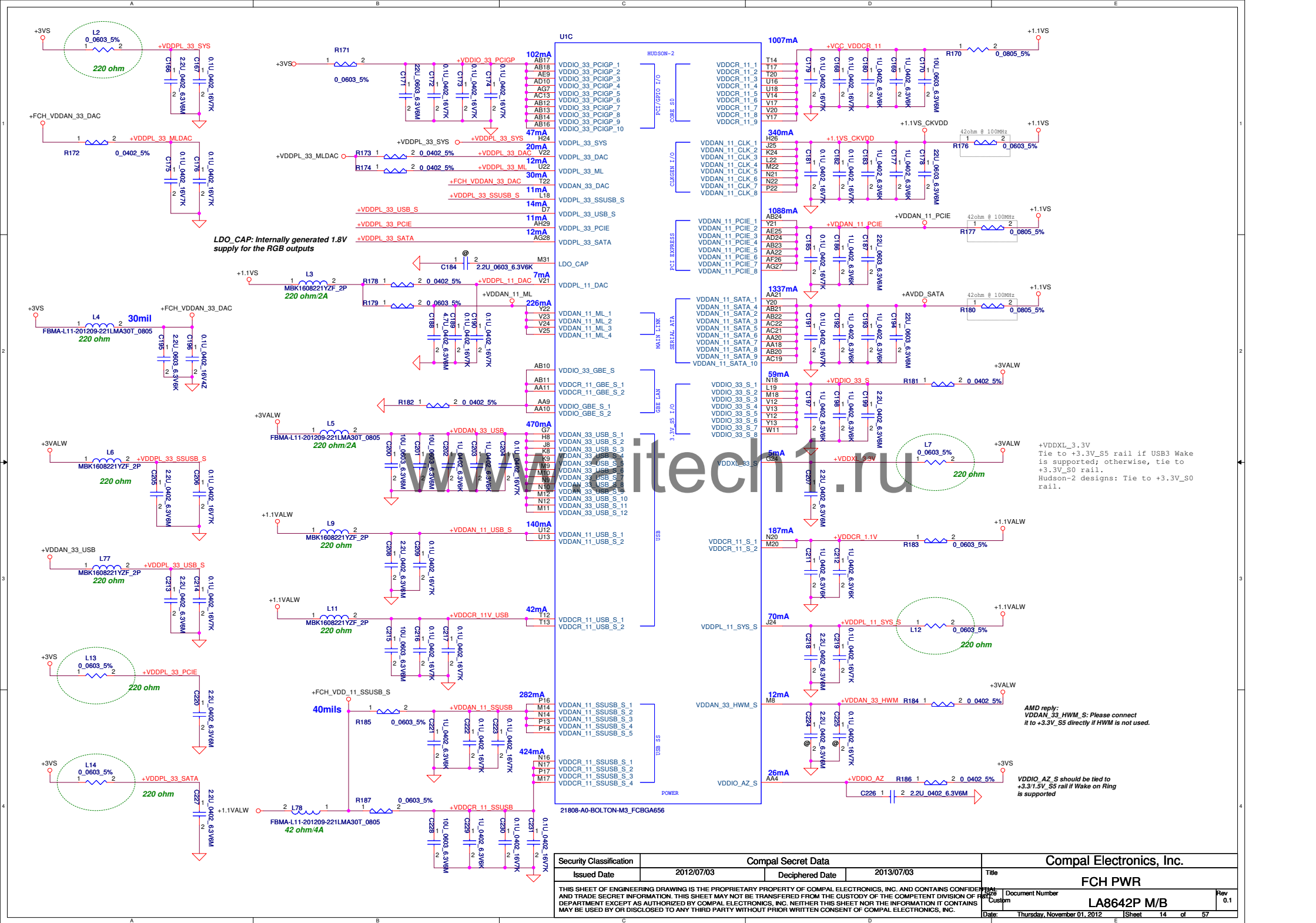


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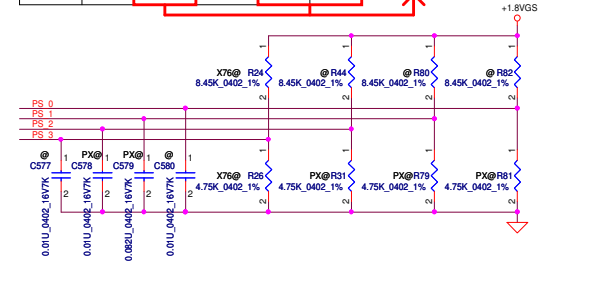
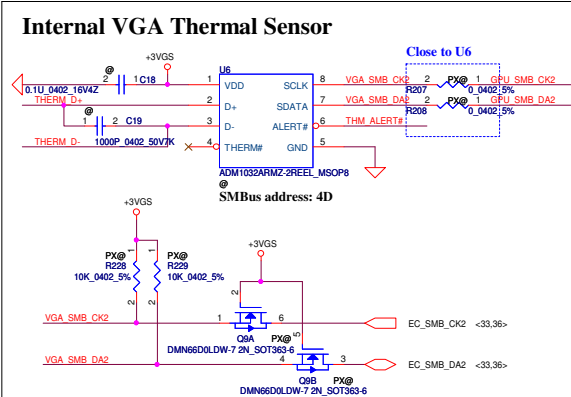


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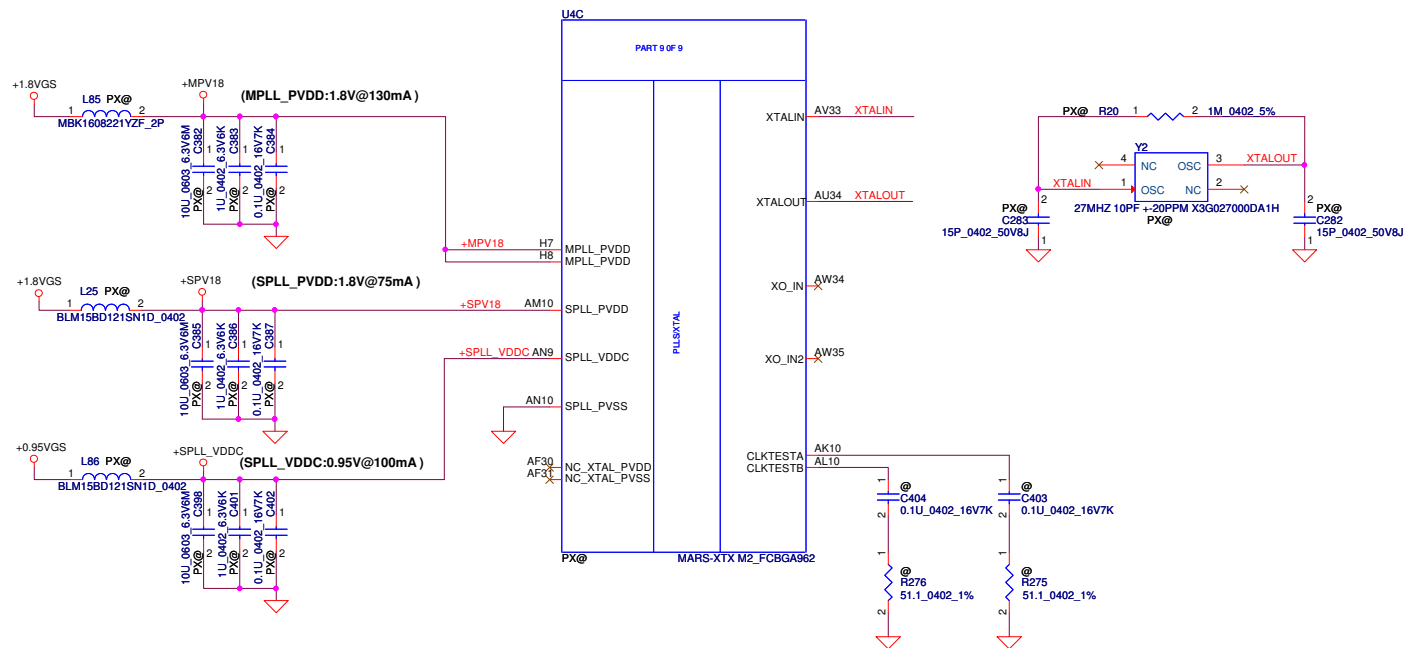


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| Size | C | Document Number | L86422 M/B | Rev | 0.1 |
| Revision Description: Microsheet 01 2012_07-2013-07 Page 1 of 63 | | | | | |

| | | |
|-----------|---------|--------|
| MPLL_PVDD | MarsCRB | Design |
| 220ohm | 1 | 1 |
| 0.1u | 1 | 1 |
| 1u | 1 | 1 |
| 10u | 1 | 1 |

| | | |
|-----------|---------|--------|
| SPLL_PVDD | MarsCRB | Design |
| 120ohm | 1 | 1 |
| 0.1u | 1 | 1 |
| 1u | 1 | 1 |
| 10u | 1 | 1 |

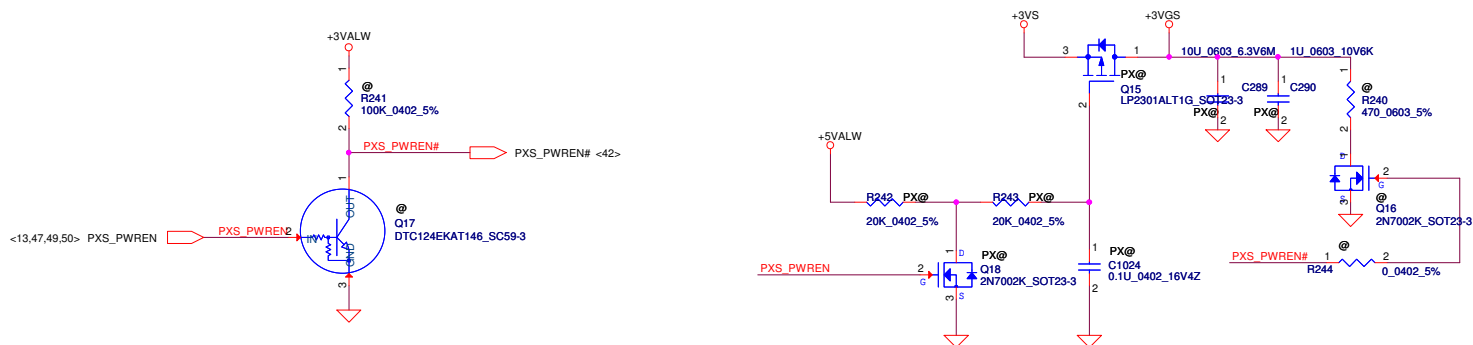
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|-----------|---------|--------|
| SPLL_VDDC | MarsCRB | Design |
| 120ohm | 1 | 1 |
| 0.1u | 1 | 1 |
| 1u | 1 | 1 |
| 10u | 1 | 1 |



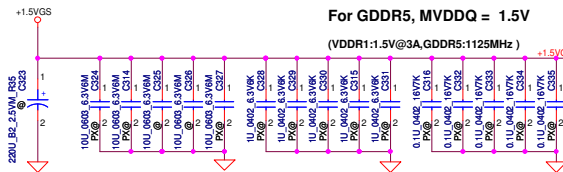
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<+3VS TO +3VGS>

Need OPEN



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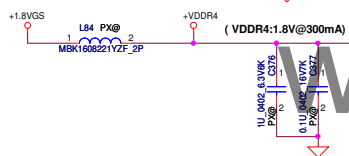
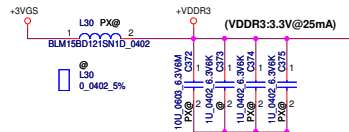
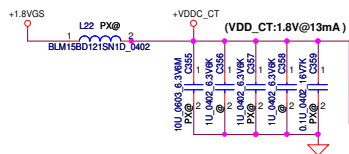


| VDDR1 | MarsCRB | Design |
|-------|---------|--------|
| 0.01u | 5 | 0 |
| 0.1u | 5 | 5 |
| 1u | 0 | 5 |
| 2.2u | 5 | 0 |
| 10u | 3 | 5 |
| 220u | 0 | 1 |

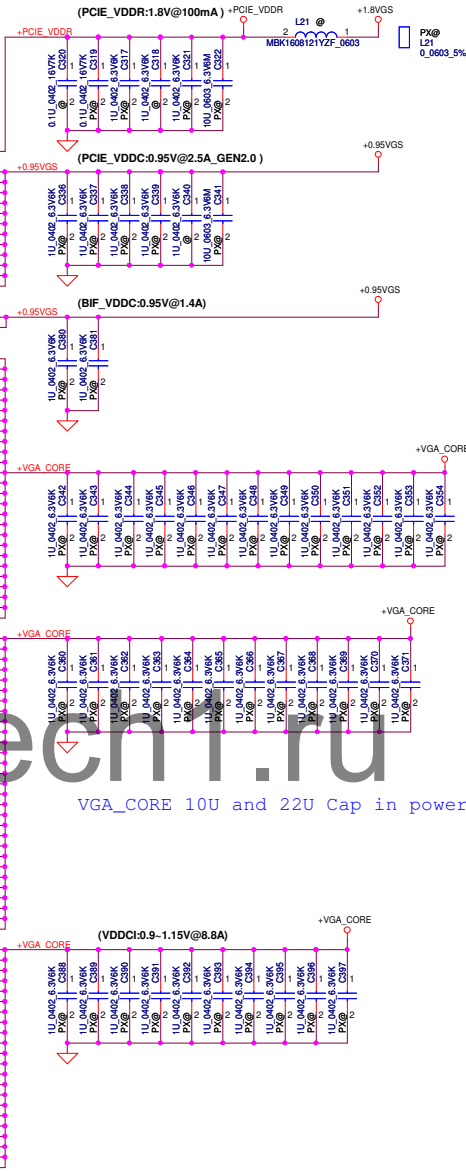
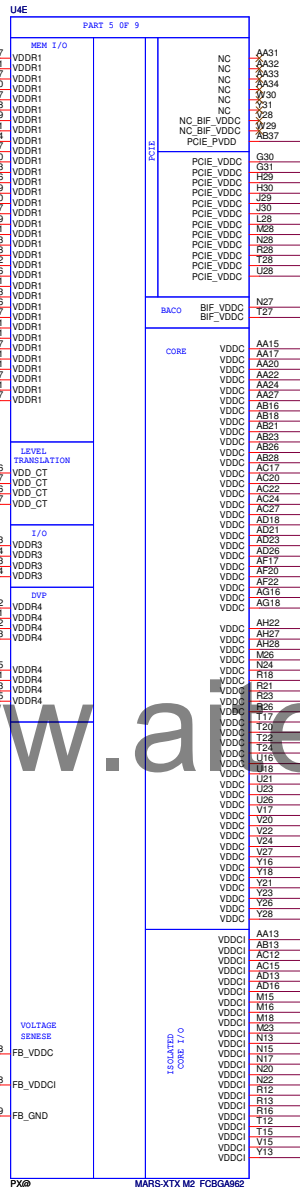
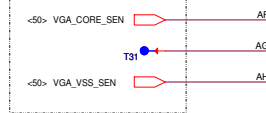
| VDD_CT | MarsCRB | Design |
|--------|---------|--------|
| 120ohm | 1 | 1 |
| 0.1u | 1 | 1 |
| 1u | 1 | 3 |
| 10u | 1 | 1 |

| VDDR3 | MarsCRB | Design |
|--------|---------|--------|
| 120ohm | 1 | 0 |
| 0.1u | 1 | 0 |
| 1u | 2 | 3 |
| 10u | 0 | 1 |

| VDDR4 | MarsCRB | Design |
|--------|---------|--------|
| 220ohm | 1 | 1 |
| 0.1u | 1 | 1 |
| 1u | 1 | 1 |
| 10u | 1 | 0 |



Route as differential pair



| PCIE_VDDR | MarsCRB | Design |
|-----------|---------|--------|
| 0.1u | 0 | 2 |
| 1u | 2 | 3 |
| 10u | 1 | 1 |

| PCIE_VDDC | MarsCRB | Design |
|-----------|---------|--------|
| 1u | 7 | 5 |
| 10u | 2 | 1 |

| VDDC | MarsCRB | Design |
|------|---------|--------|
| 0.1u | 2 | 0 |
| 1u | 0 | 25 |
| 2.2u | 16 | 0 |
| 10u | 9 | 3 |
| 22u | 0 | 2 |
| 470u | 4 | 5 |

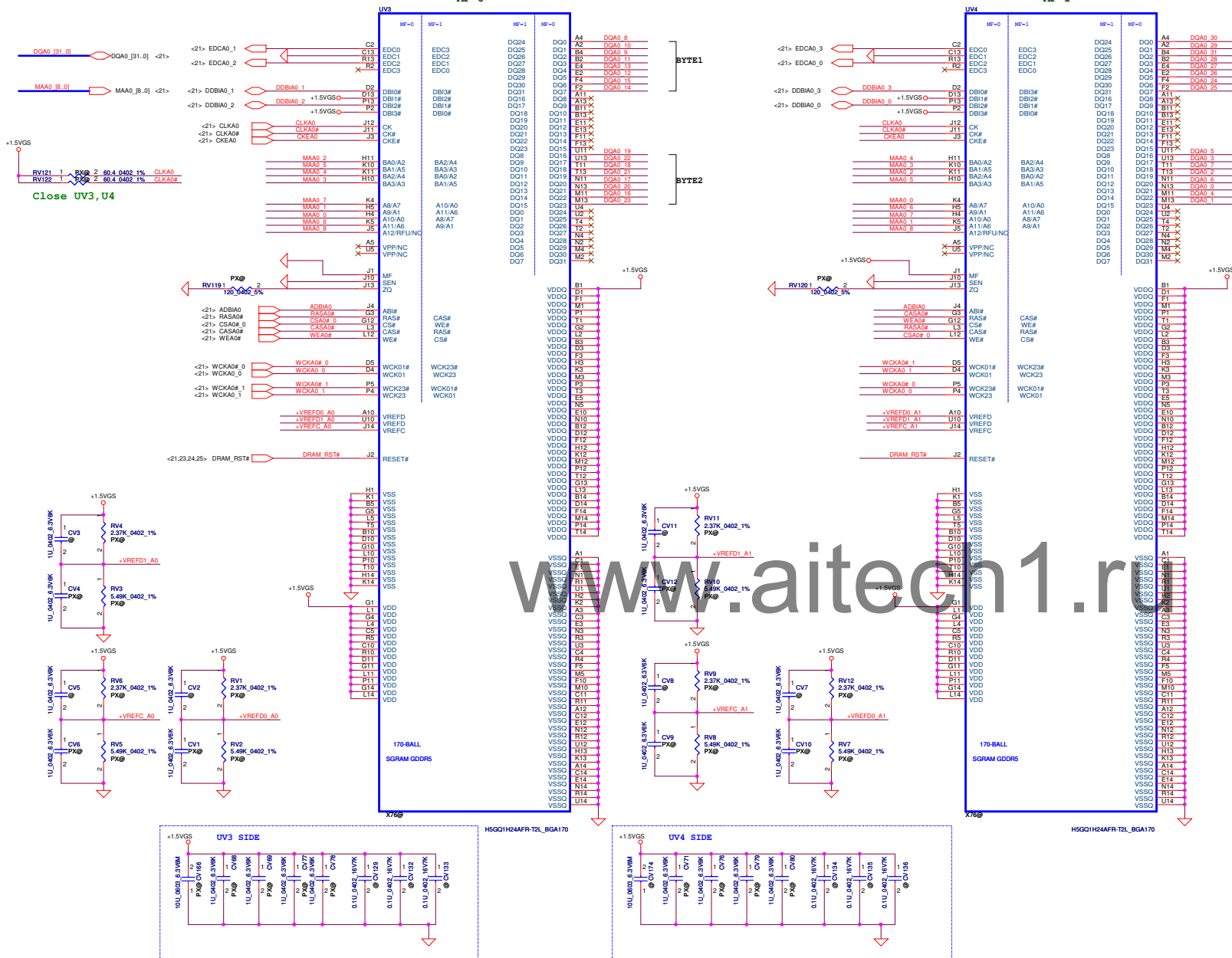
| VDDCI | MarsCRB | Design |
|-------|---------|--------|
| 0.1u | 3 | 0 |
| 1u | 3 | 9 |
| 2.2u | 1 | 0 |
| 10u | 1 | 2 |
| 22u | 0 | 1 |
| 470u | 1 | 0 |

VGA_CORE 10U and 22U Cap in power side sheet

Memory Partition A - Lower 32 bits

MF=0

MF=1

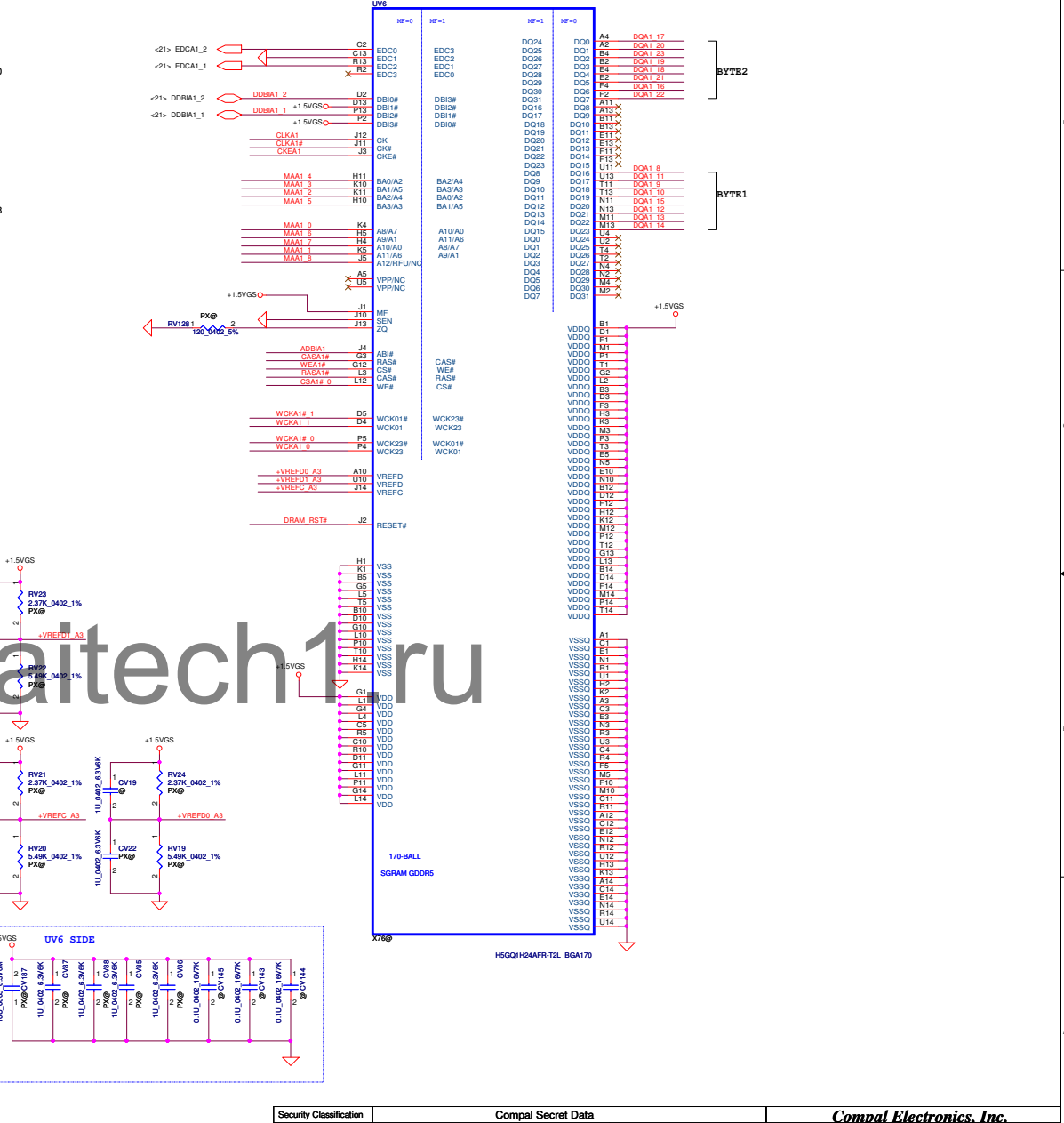
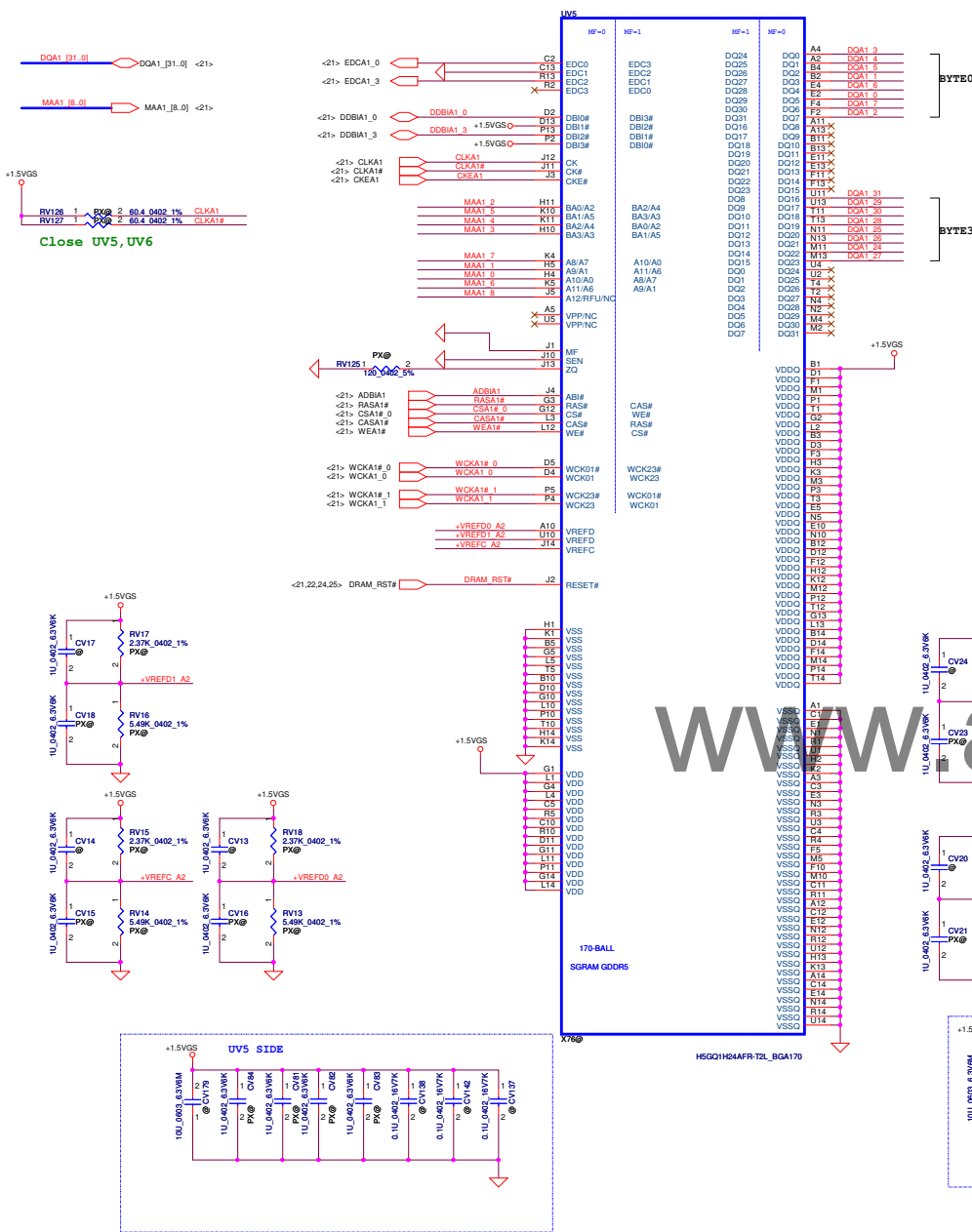


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| Date | | Thursday, November 01, 2012 | 1.0 |
| Sheet | | 22 | of 57 |

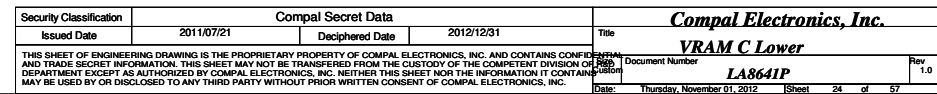
Memory Partition A - Upper 32 bits

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MF=1



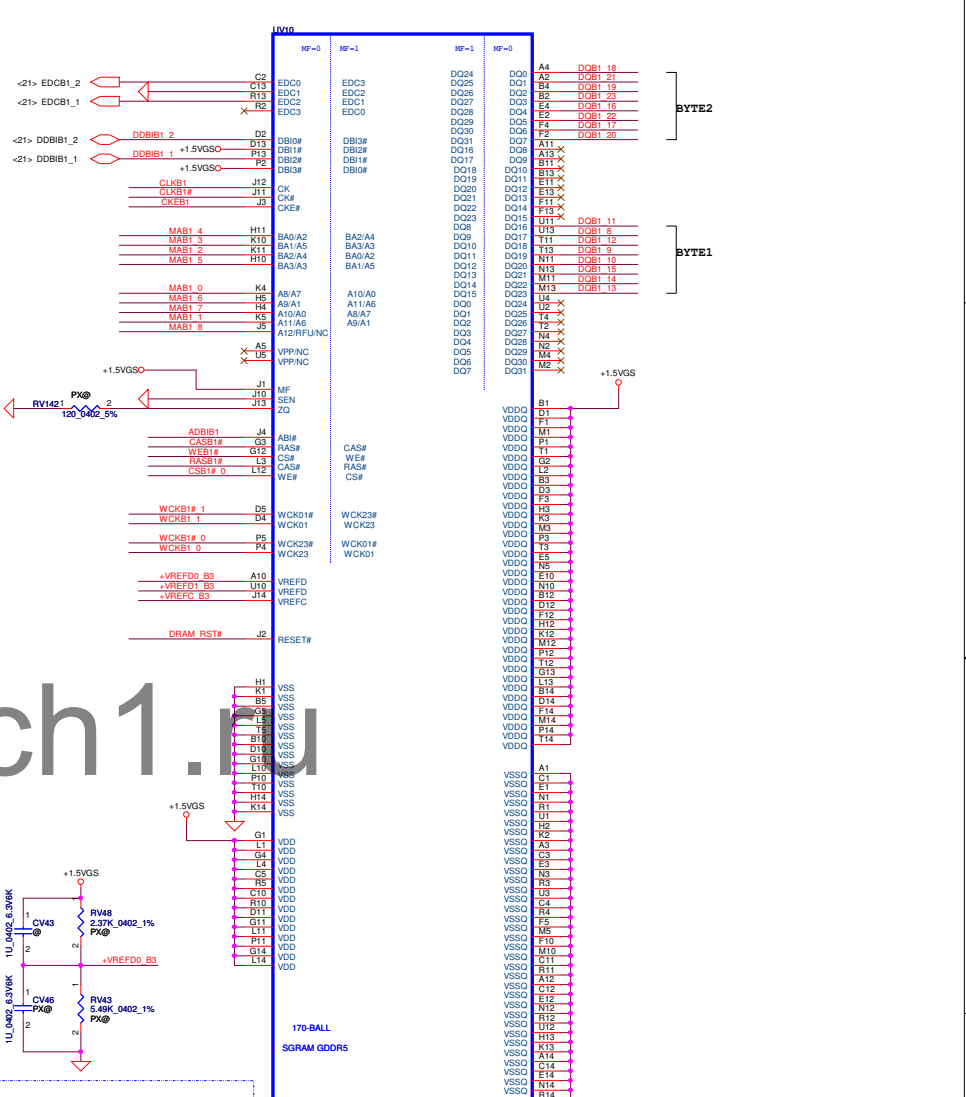
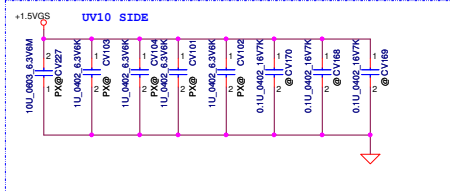
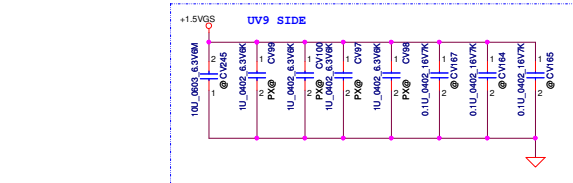
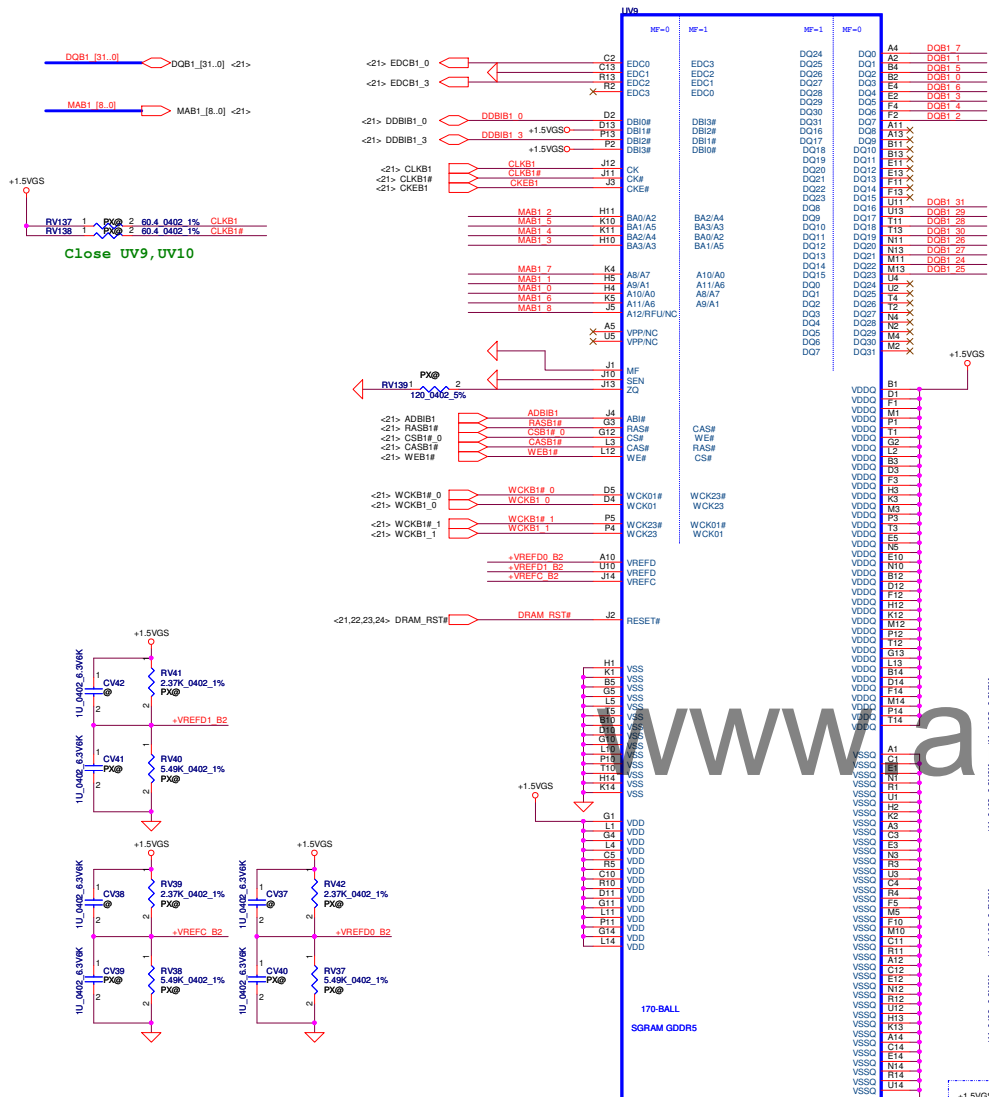
| Security Classification | | Compal Secret Data | | Compal Electronics, Inc. | |
|---|-----------------|--------------------|------------|--------------------------|--------------|
| Issued Date | 2011/07/21 | Deciphered Date | 2012/12/31 | Title | VRAM A Upper |
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| Size | Document Number | LA8641P | | | Date |
| Thursday, November 01, 2012 | Sheet | 29 | of | 57 | |

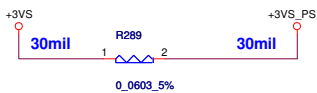
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Memory Partition C - Upper 32 bits

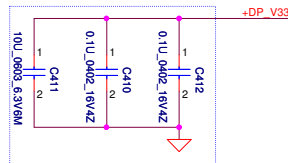
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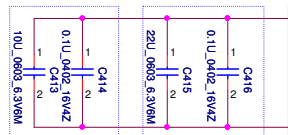




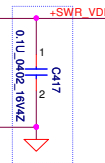
Close to Pin3



Close to L87

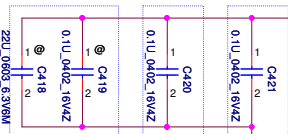


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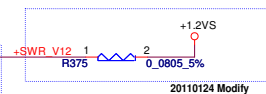
Close to Pin13

Close to L29

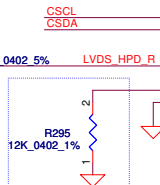
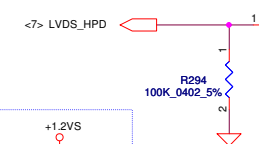


Close to Pin27

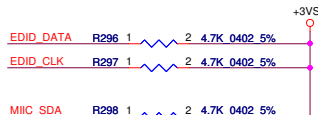
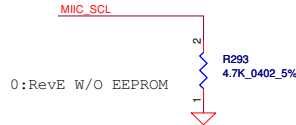
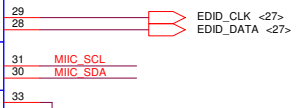
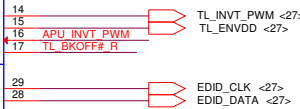
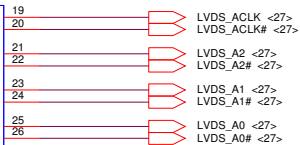
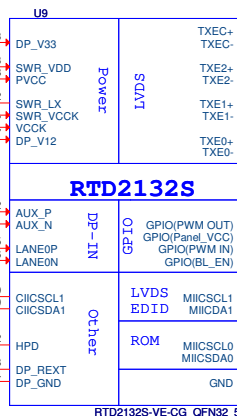
Close to Pin7



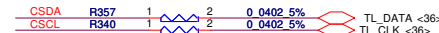
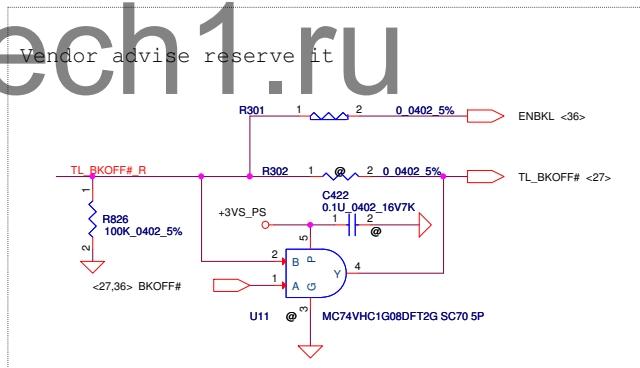
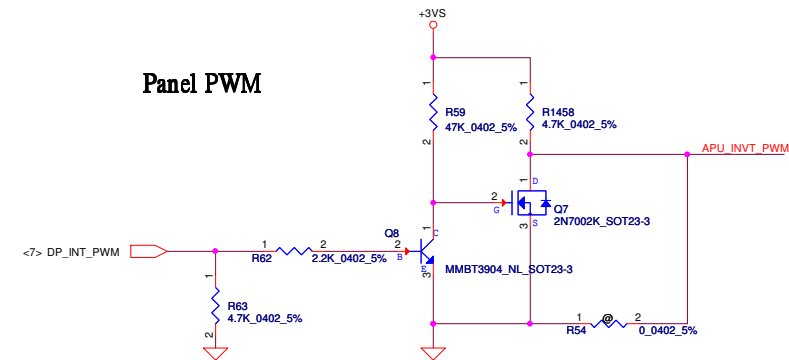
Reserved for EC programming ROM
Need EC confirm



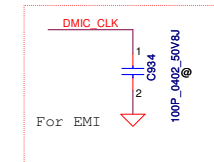
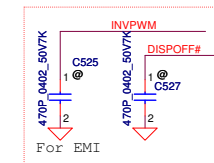
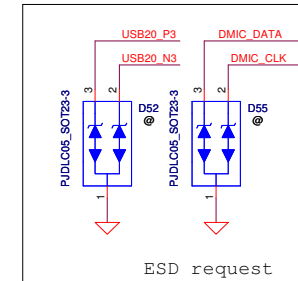
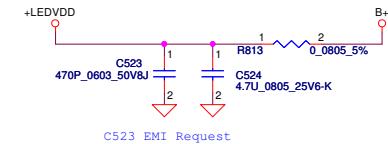
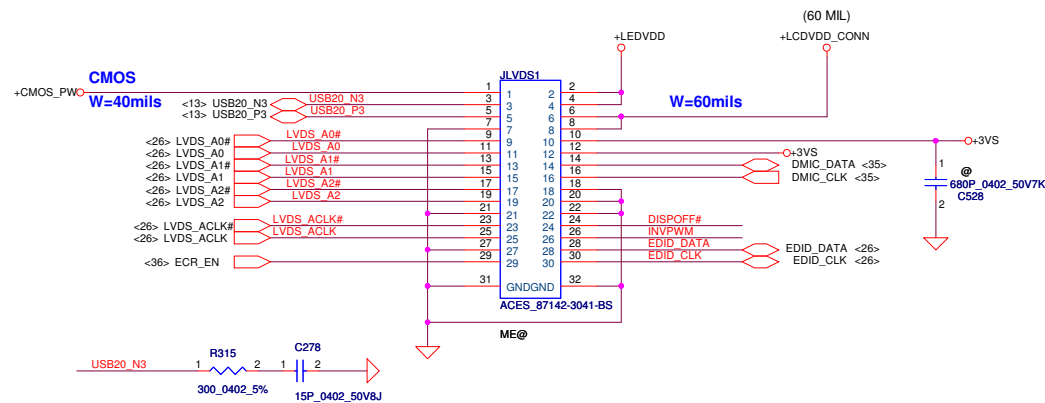
Change to 12Kohm 1% (DG ref.)
20101114



Panel PWM

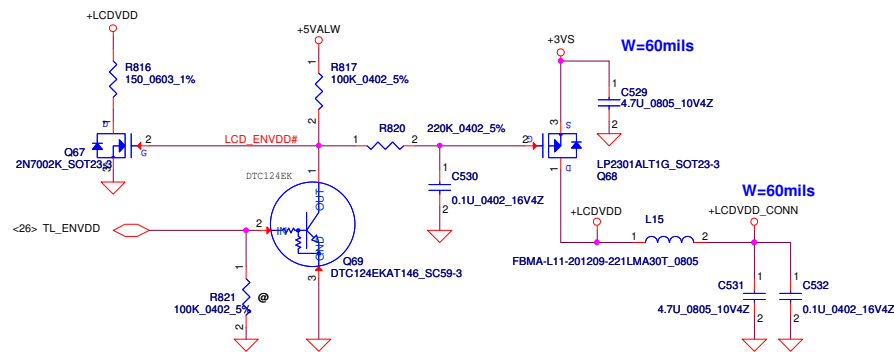


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| Date | | | | Thursday, November 01, 2012 | | | | Rev | | | |
| Sheet | | | | 26 | | | | of | | | |
| 57 | | | | 0.1 | | | | | | | |

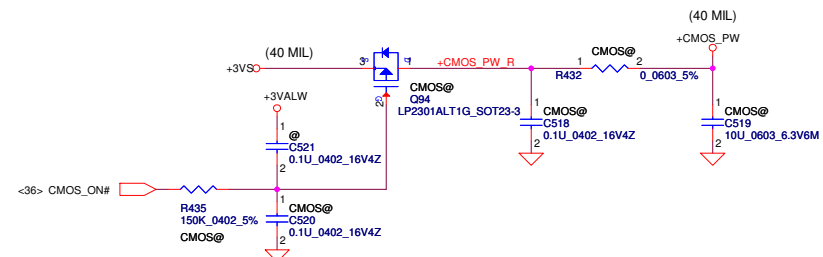


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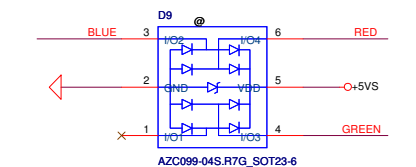
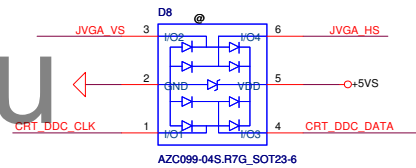
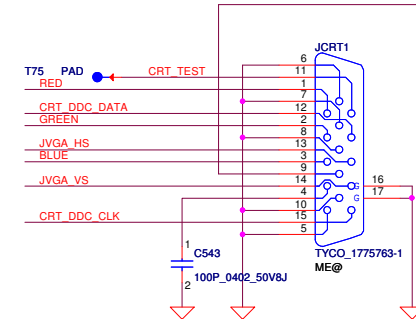
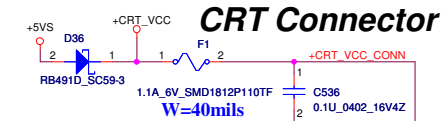
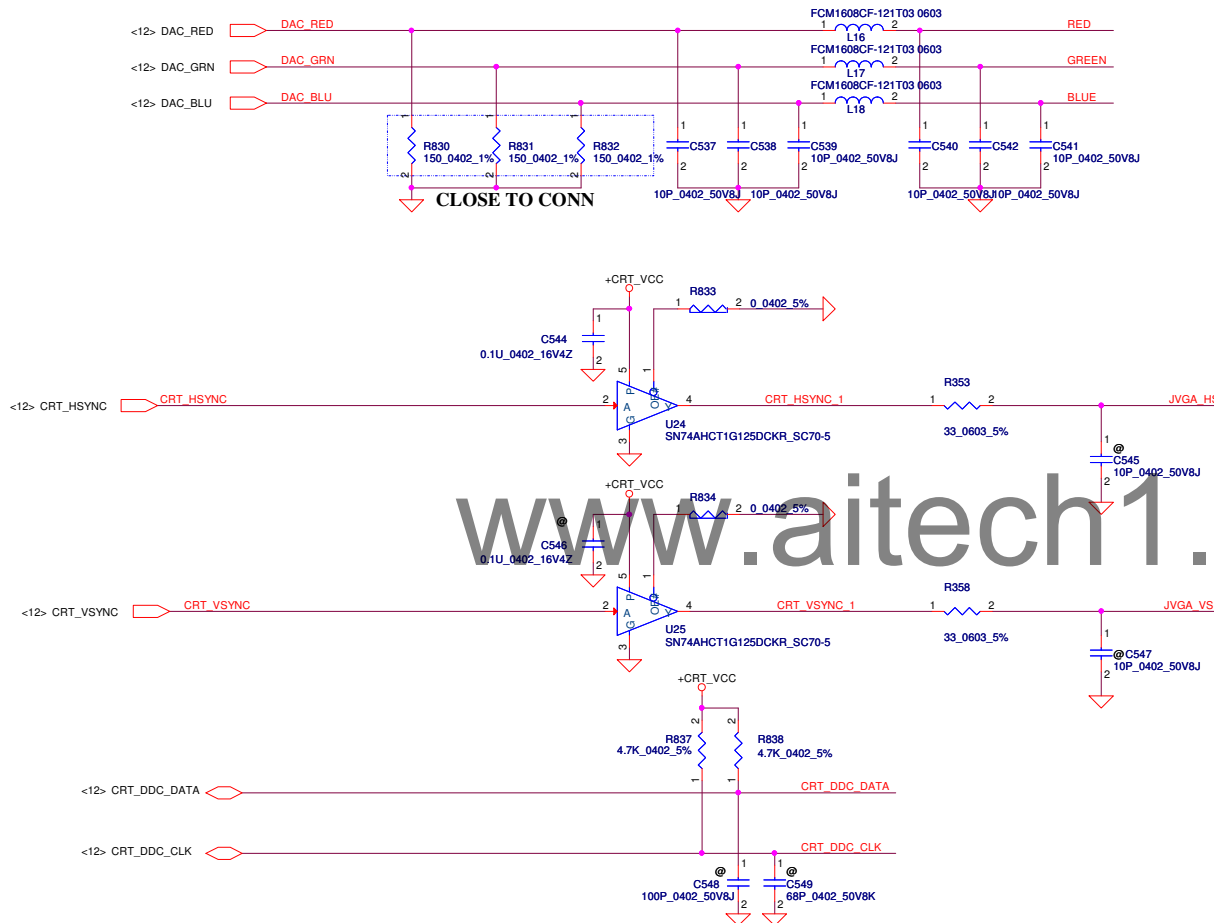
LVDS POWER CIRCUIT



CMOS Camera



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|--|------------|--------------------|------------|-----------------------------------|----------------|
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| Issued Date | | 2012/07/03 | | Deciphered Date | | 2013/07/03 | | Title | | | |
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| | | | | | | Document Number | | LA8642P M/B | | Rev | |
| | | | | | | Custom | | | | 0.1 | |
| | | | | | | Date | | Thursday, November 01, 2012 | | Sheet 28 of 57 | |
| | | | | | | | | | | | |

Compal Electronics, Inc.

CRT Connector

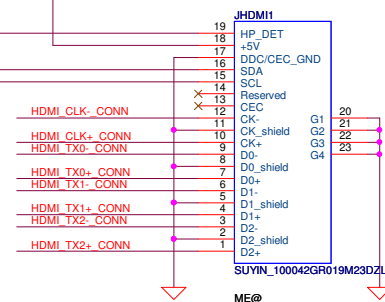
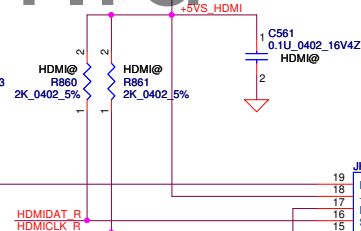
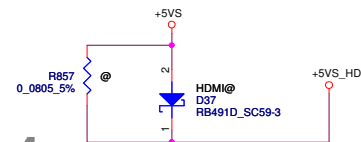
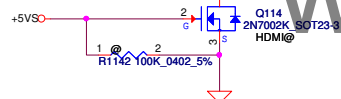
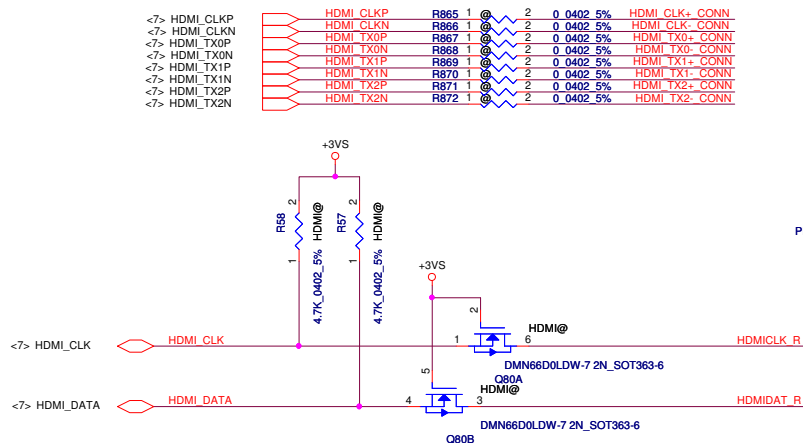
LA8642P M/B

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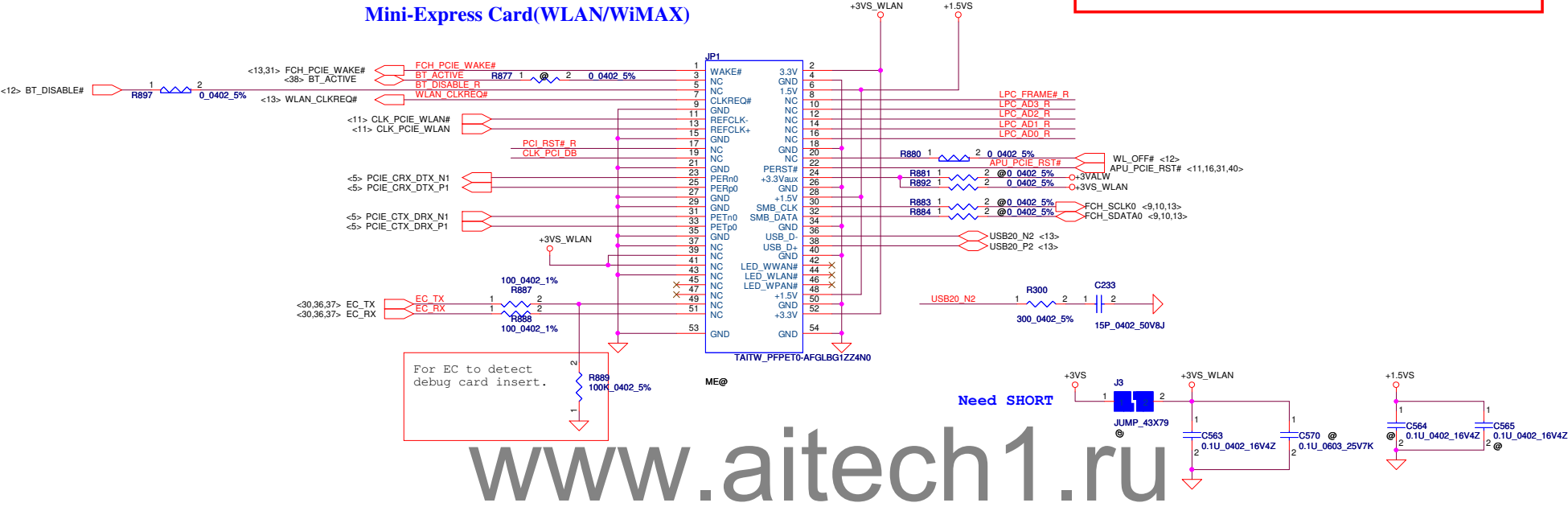
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| Security Classification | | Compal Secret Data | | Compal Electronics, Ltd. | |
| Issued Date | 2012/07/03 | Deciphered Date | 2013/07/03 | Title | HDMI CONN |
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| | | | | Date: Thursday, November 01, 2012 | Sheet 29 of 57 |

Mini-Express Card for WLAN/WiMAX(Half)
Mini-Express Card for SSD(Full)

Reserve for SW mini-pcie debug card.
Series resistors closed to KBC side.

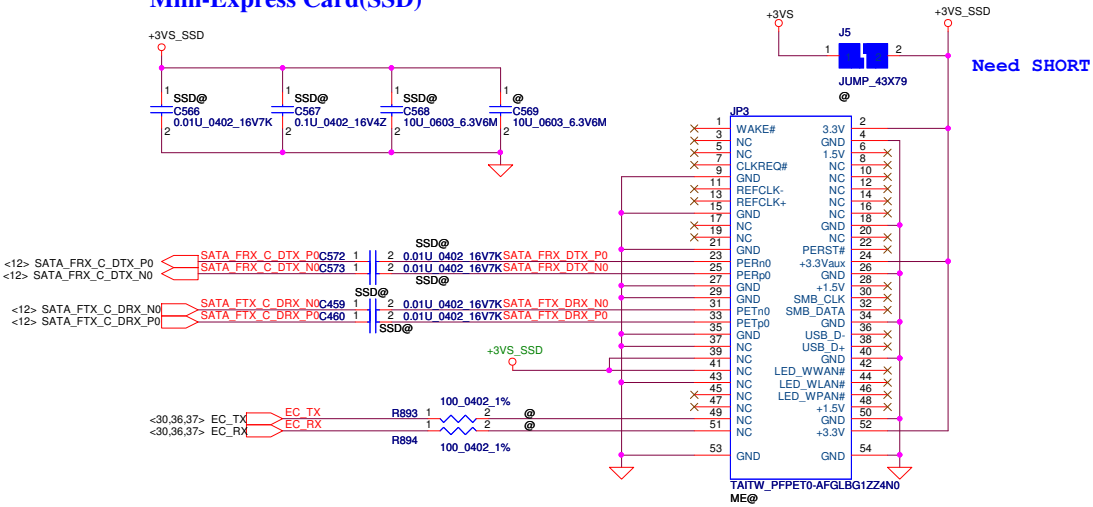
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| LPC_AD3_R | R874 | 1 | 2 | 0 | 0402_5% | LPC_AD3 | LPC_AD3 <11,36> |
| LPC_AD2_R | R875 | 1 | 2 | 0 | 0402_5% | LPC_AD2 | LPC_AD2 <11,36> |
| LPC_AD1_R | R876 | 1 | 2 | 0 | 0402_5% | LPC_AD1 | LPC_AD1 <11,36> |
| LPC_AD0_R | R877 | 1 | 2 | 0 | 0402_5% | LPC_AD0 | LPC_AD0 <11,36> |
| CLK_PCI_DB | R879 | 1 | 2 | 0 | 0402_5% | CLK_PCI_DB | CLK_PCI_DB <11> |

Mini-Express Card(WLAN/WiMAX)



Mini-Express Card(SSD)

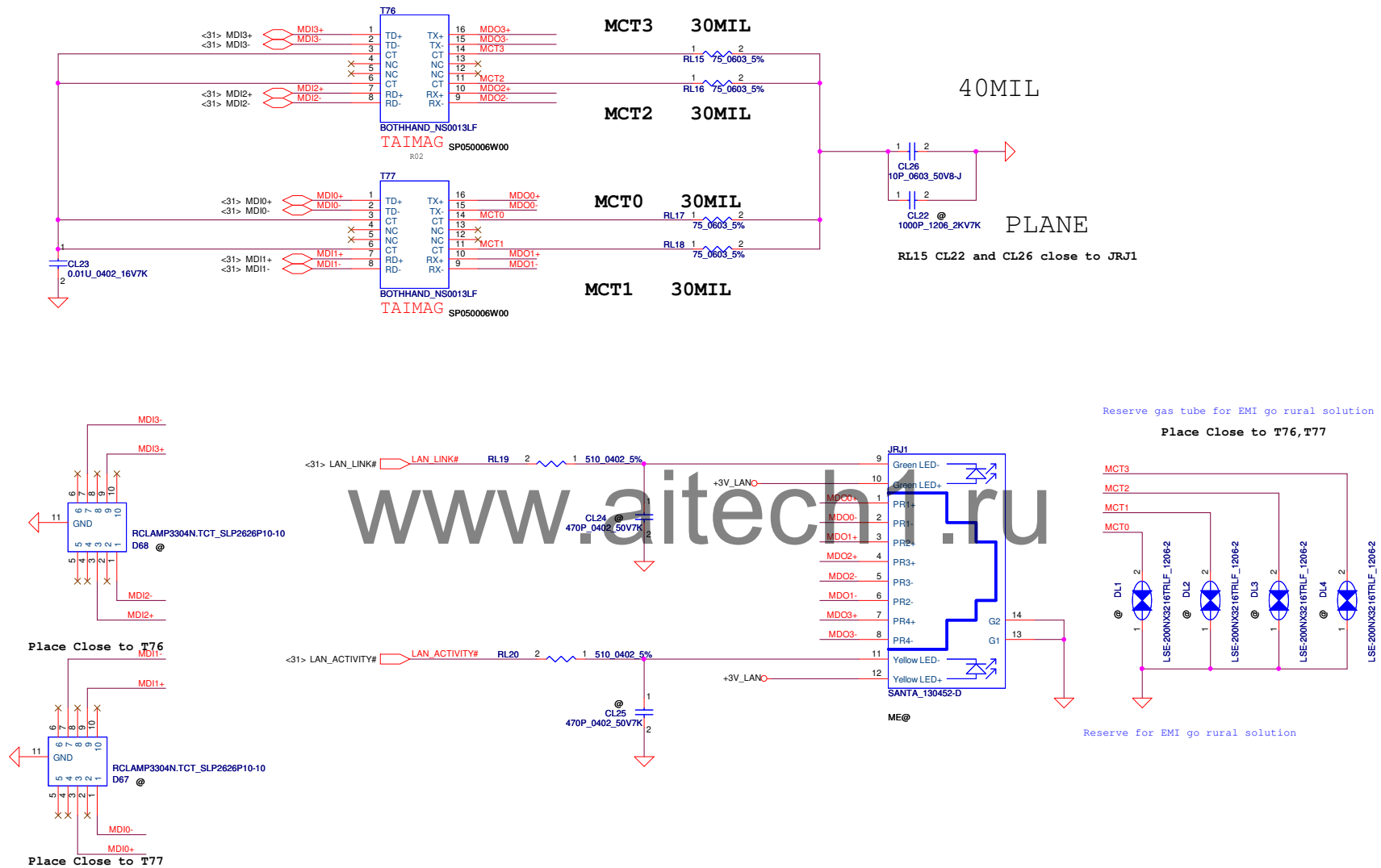
SSD Active:4.5W(1.5A)



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|---|--------------------|-----------------|------------|----------------|
| Issued Date | 2012/07/03 | Deciphered Date | 2013/07/03 | Mini-Card |
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| Date: Thursday, November 01, 2012 | | | | Sheet 30 of 57 |



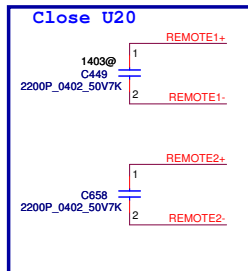
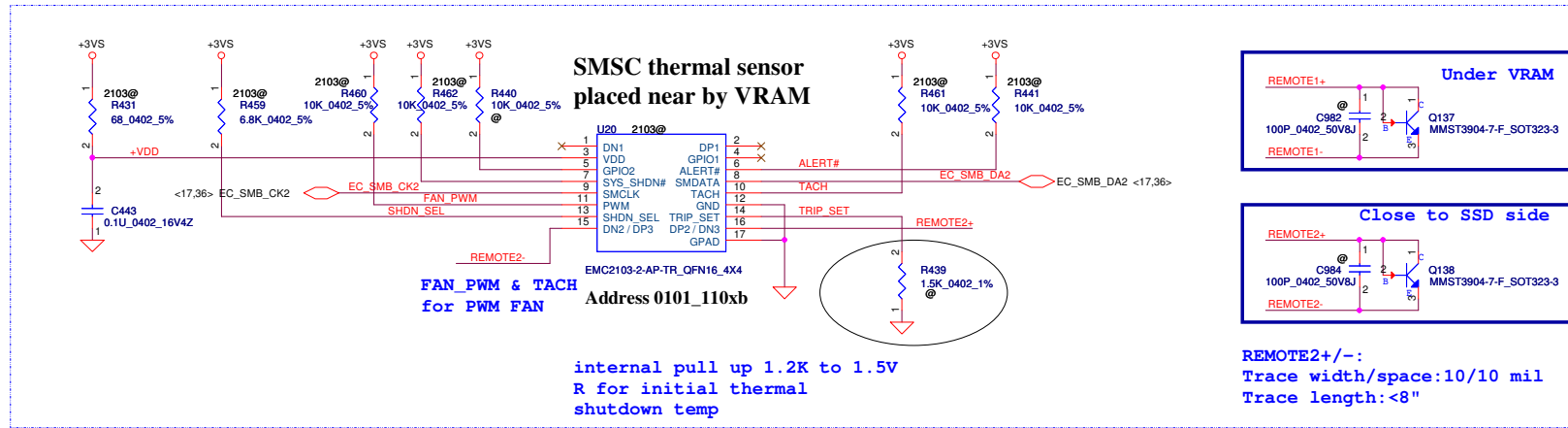
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| Issued Date | 2012/07/03 | Deciphered Date | 2013/07/03 | Title | LAN-RTL8111F/8105E |
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| | | | | Date | Thursday, November 01, 2012 |
| | | | | Sheet | 32 of 57 |

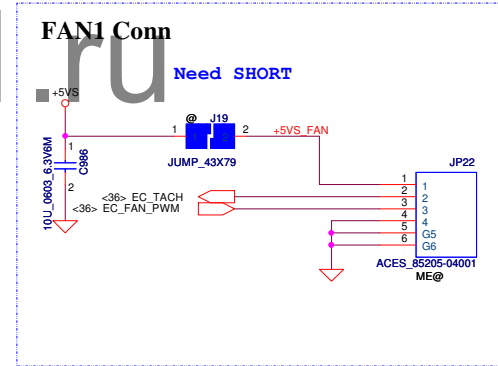
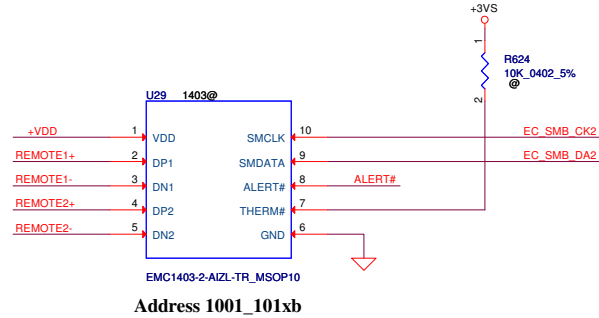
R431 1403@
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1403:
@C982/@C984=100p

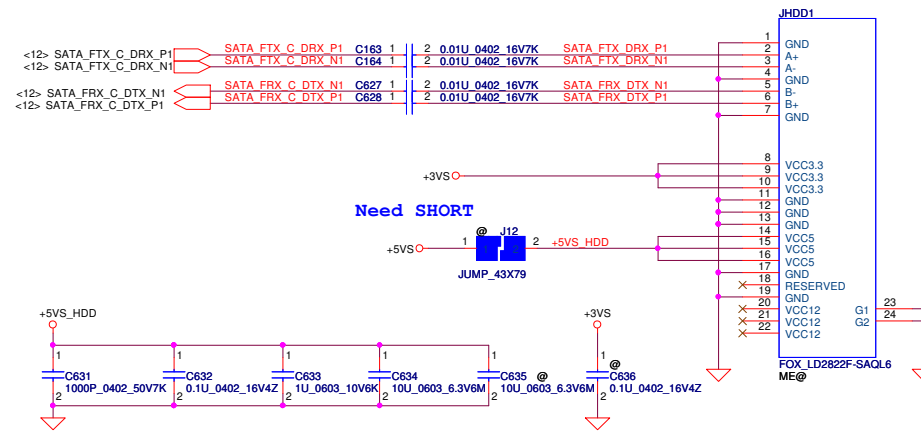


| Shutdown Temp | TRIP SET R1387 (1%) |
|---------------|---------------------|
| 93 | 953ohm |
| 94 | 1020ohm |
| 95 | 1100ohm |
| 96 | 1150ohm |
| 97 | 1240ohm |
| 98 | 1330ohm |
| 99 | 1400ohm |
| 100 | 1500ohm |
| 101 | 1580ohm |
| 102 | 1690ohm |
| 103 | 1820ohm |
| 104 | 1960ohm |
| 105 | 2050ohm |

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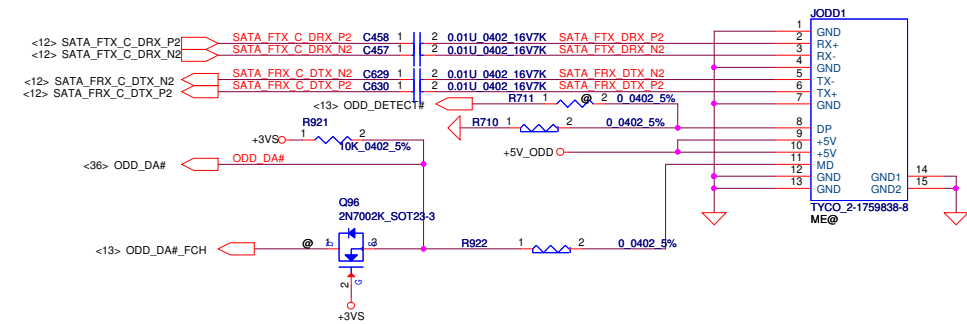
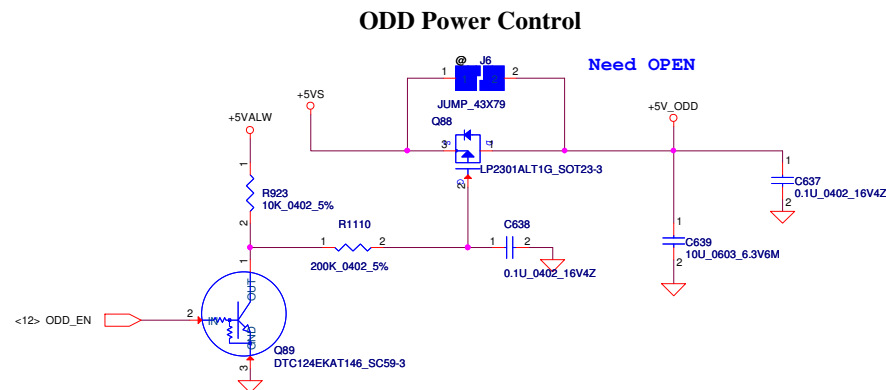


SATA HDD Conn.

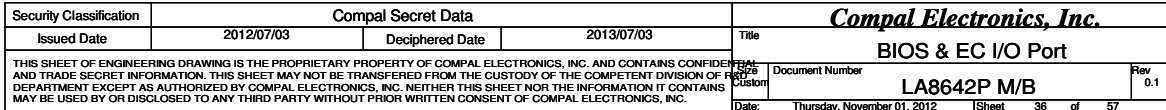


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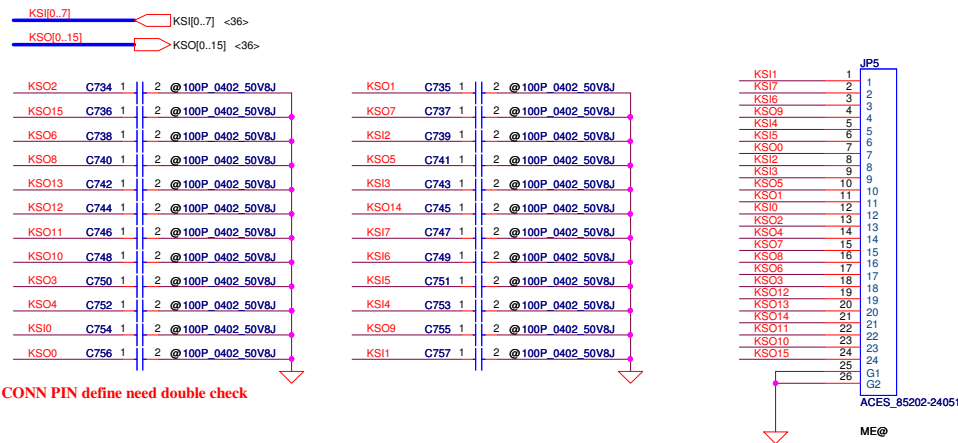
SATA ODD Conn.



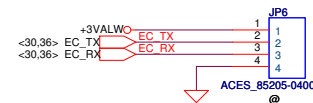
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| | | | | | | Document Number | | Rev | |
| | | | | | | Custom | | 0.1 | |
| | | | | | | LA8642P M/B | | | |
| Date: Thursday, November 01, 2012 | | | | | | Sheet 34 of 57 | | | |



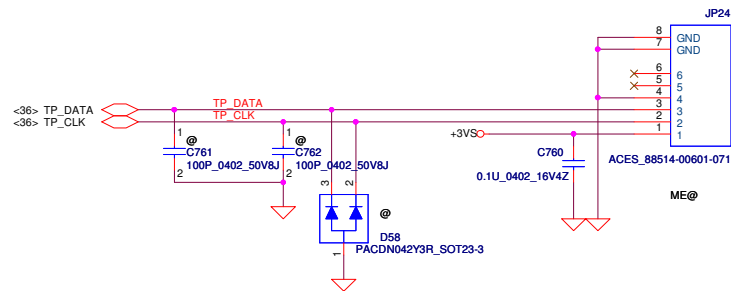
INT_KBD Conn.



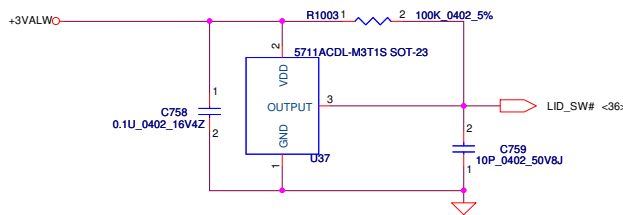
EC DEBUG PORT

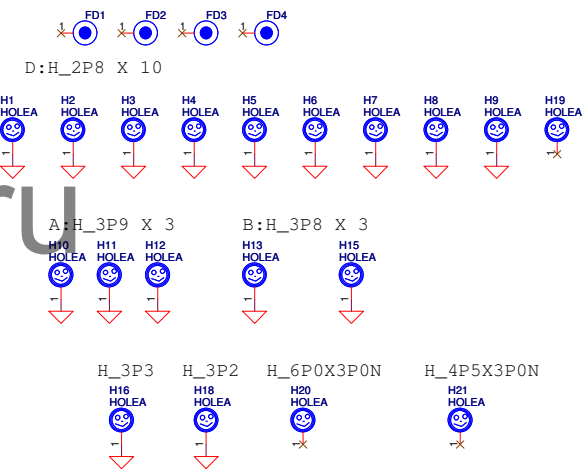
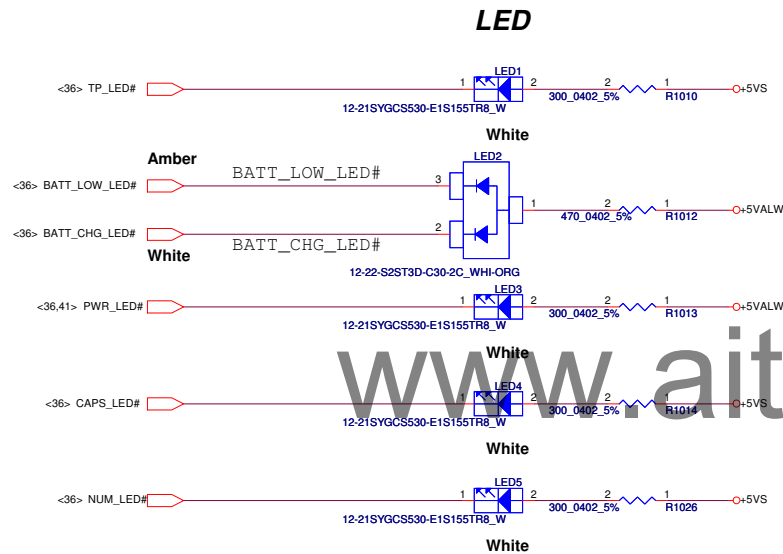


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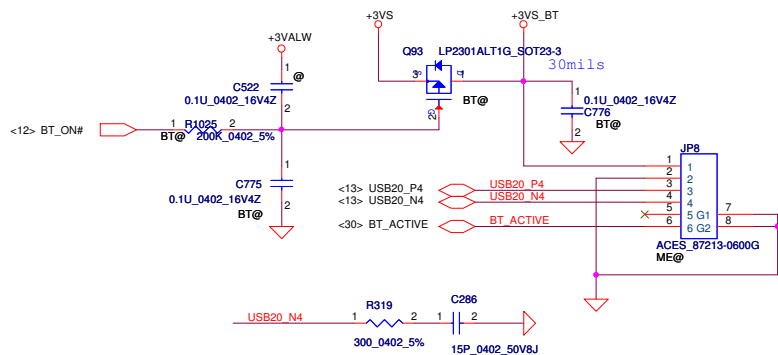


Lid Switch



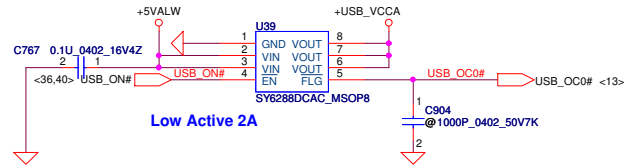


BT MODULE CONN

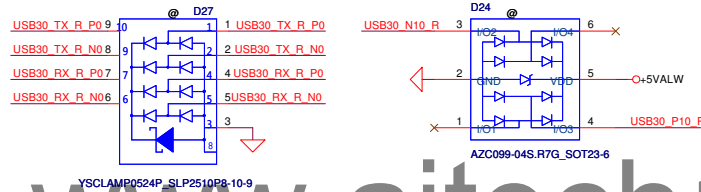


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|---|--------------------|-----------------|--------------------------|-----------------------------|
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| | | | | Rev |
| | | | | 0.1 |
| | | | | Date |
| | | | | Thursday, November 01, 2012 |
| | | | | Sheet |
| | | | | 38 of 57 |

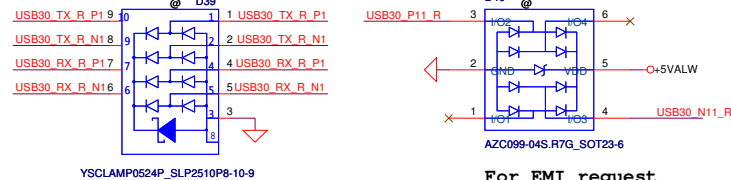
LEFT SIDE USB3.0 PORT X2



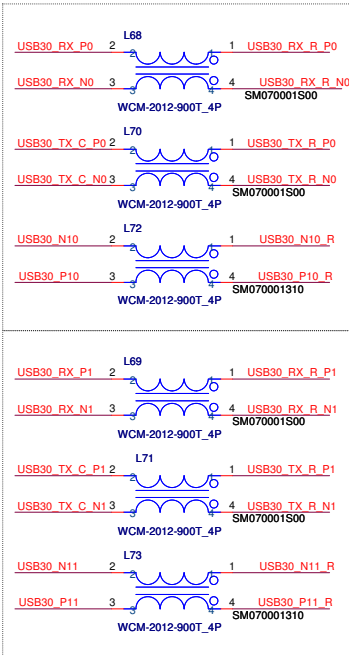
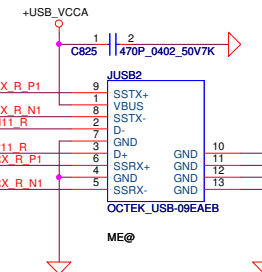
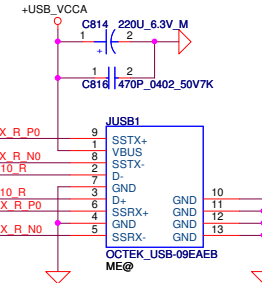
For ESD request



For ESD request

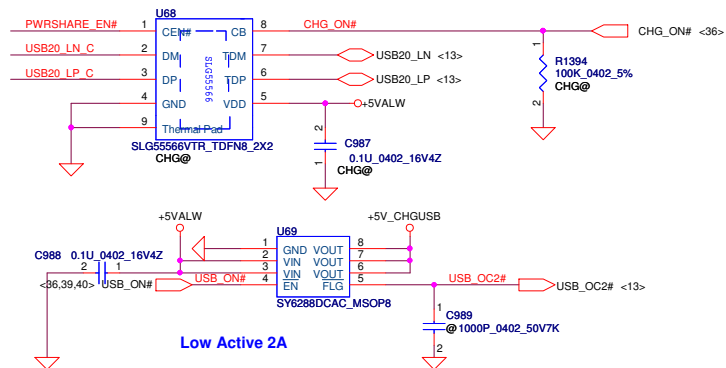


For EMI request

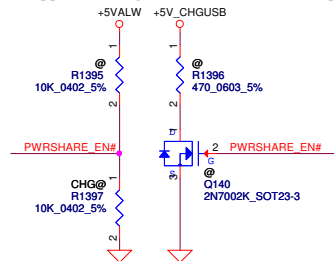


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| Issued Date | 2012/07/03 | Deciphered Date | 2013/07/03 | Title | | |
| | | | | USB3.0 ports | | |
| | | | | Document Number | LA8642P M/B | |
| | | | | Rev | 0.1 | |
| | | | | Date | Thursday, November 01, 2012 | |
| | | | | Sheet | 39 of 57 | |

Right side USB Charger

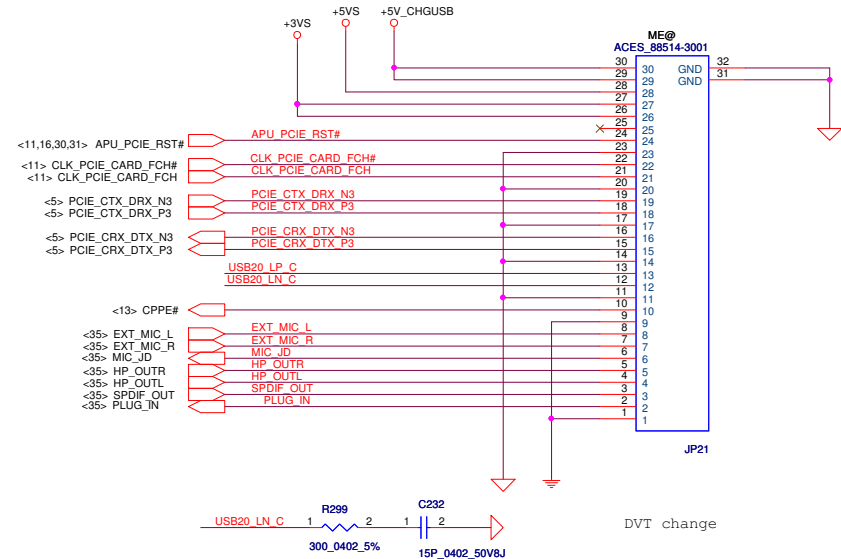


Support Charge should use another pin for Power switch control

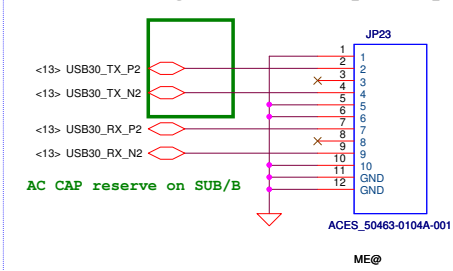


| CB | Function |
|----|---|
| L | Audio detection charger identification active |
| H | DE / DM / TDP / TSM |

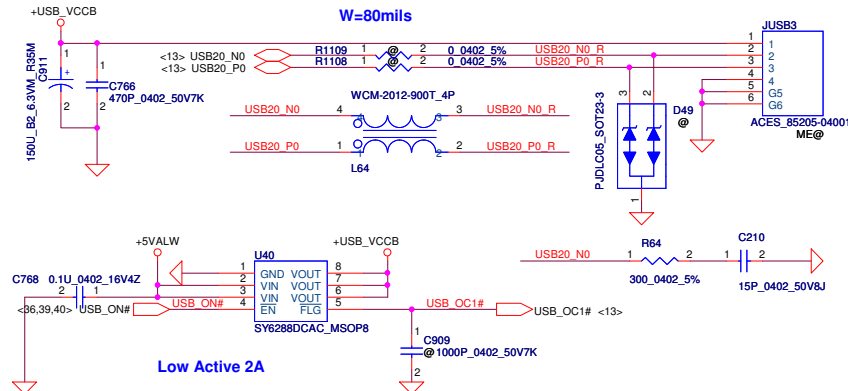
USB2.0/3.0 choke and ESD diode at sub-B.



Right side USB3.0 port (Option)

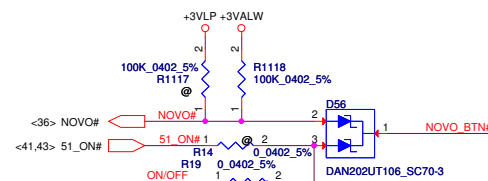
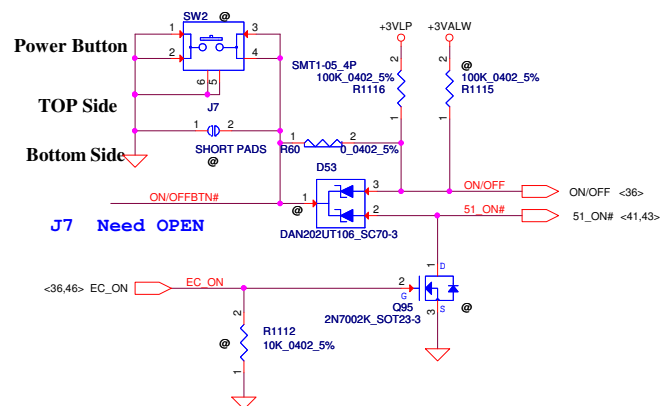


Right USB Conn.(Cable)

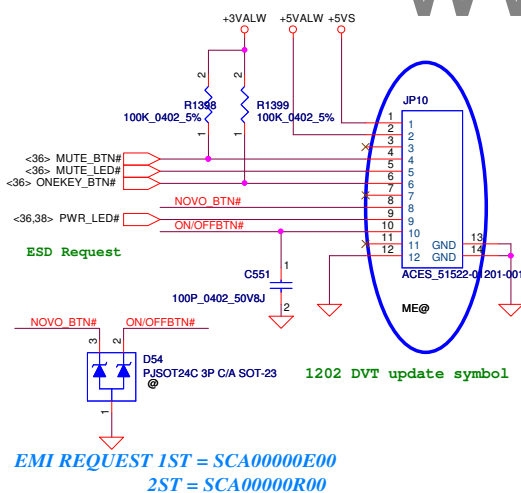
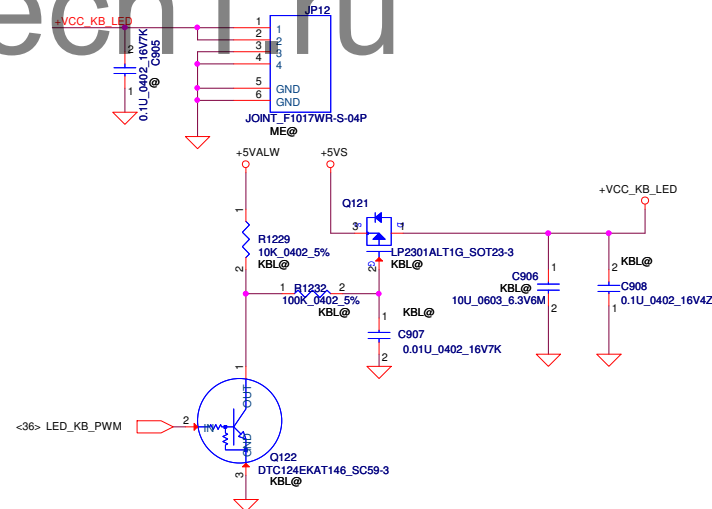


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| | | | | LA8642P M/B |
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ON/OFF switch

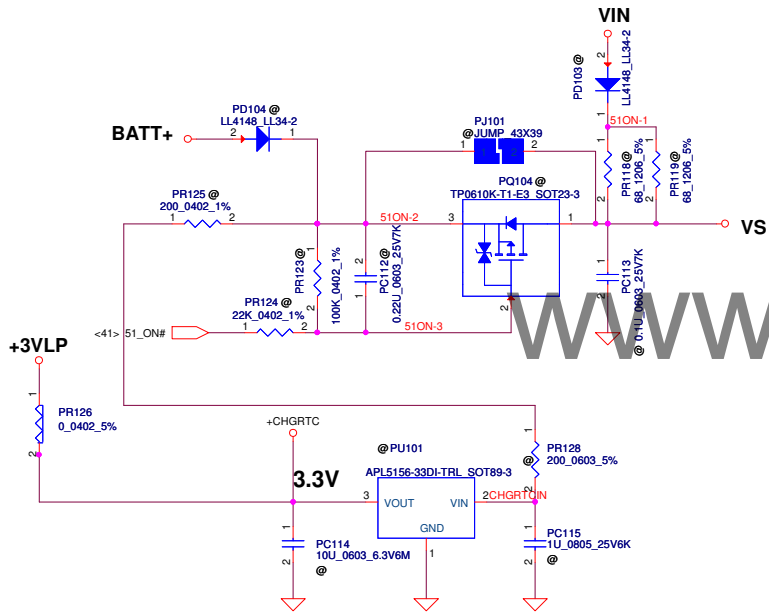
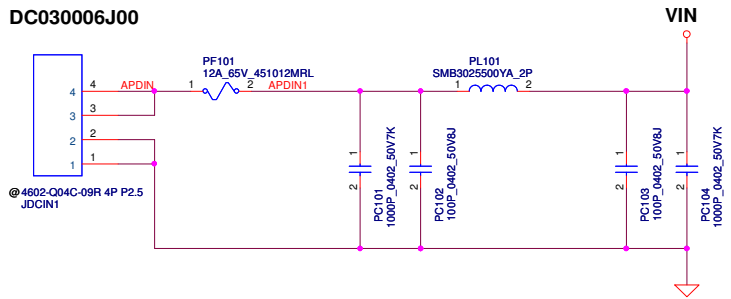


Power Button/B link to Function/B Conn. 10pin

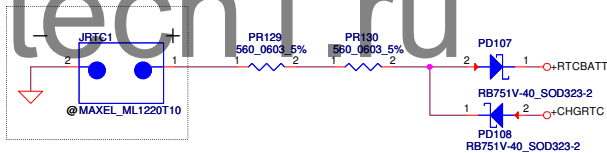
**KB Lighting CONN.4pin**

| | | | | | |
|---|------------|--------------------|------------|--|---------|
| Security Classification | | Compal Secret Data | | Compal Electronics, Ltd. Other IO connector | |
| Issued Date | 2012/07/03 | Deciphered Date | 2013/07/03 | Title Document Number JITRILAS662P M/B | Rev. 0. |
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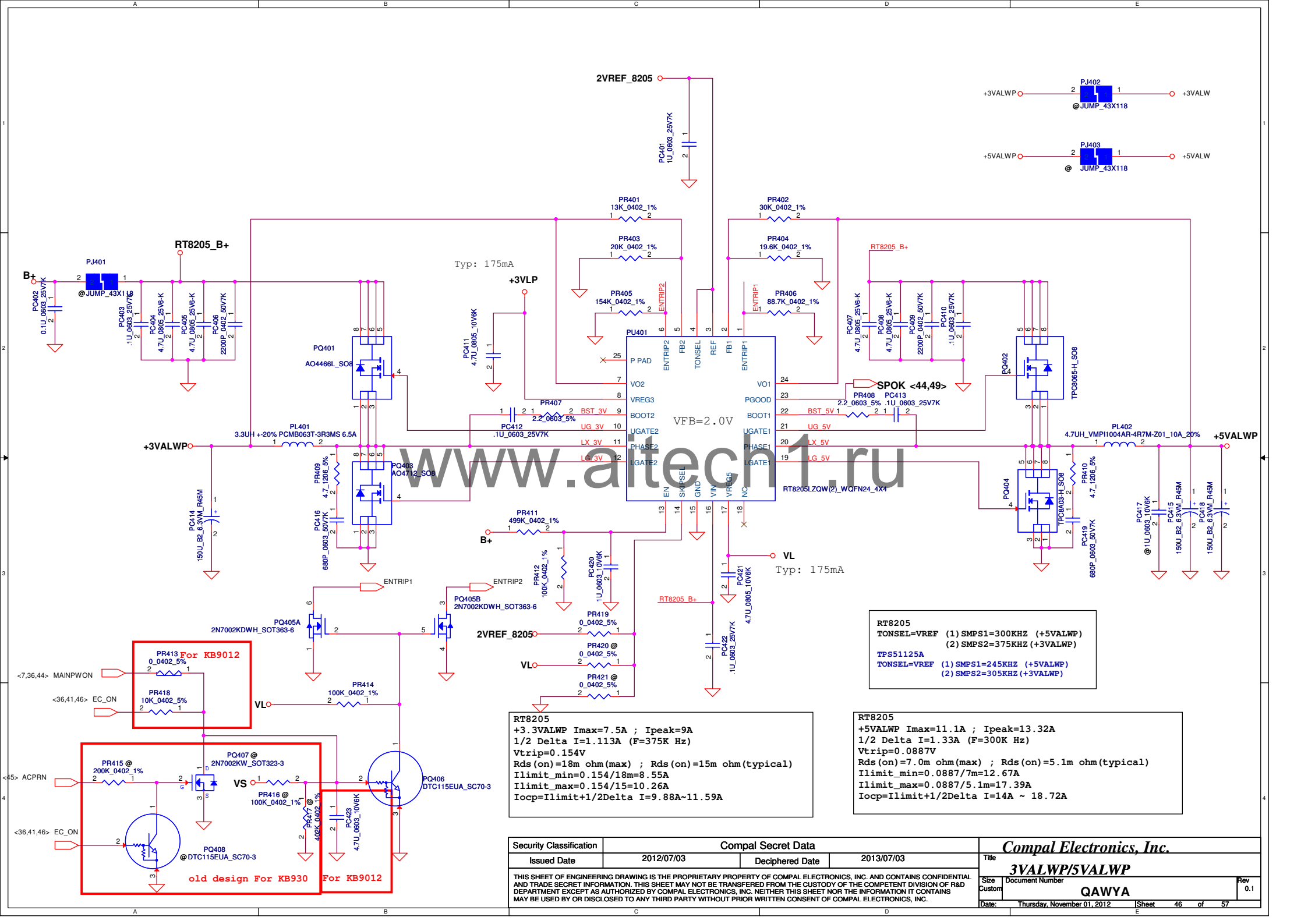
DC030006J00



RTC Battery



| | | | | | |
|--|------------|--------------------|------------|-----------------------------------|----------------|
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| | | | | QAWYA | 0.1 |
| | | | | Date: Thursday, November 01, 2012 | Sheet 43 of 57 |



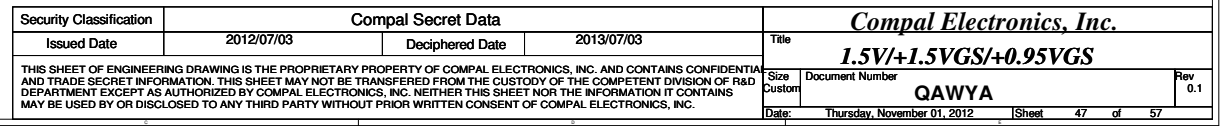
RT8205
+3.3VALWP I_{max}=7.5A ; I_{peak}=9A
1/2 Delta I=1.113A (F=375K Hz)
V_{trip}=0.154V
R_{ds(on)}=18m ohm(max) ; R_{ds(on)}=15m ohm(typical)
I_{limit_min}=0.154/18m=8.55A
I_{limit_max}=0.154/15=10.26A
I_{ocp}=I_{limit}+1/2Delta I=9.88A~11.59A

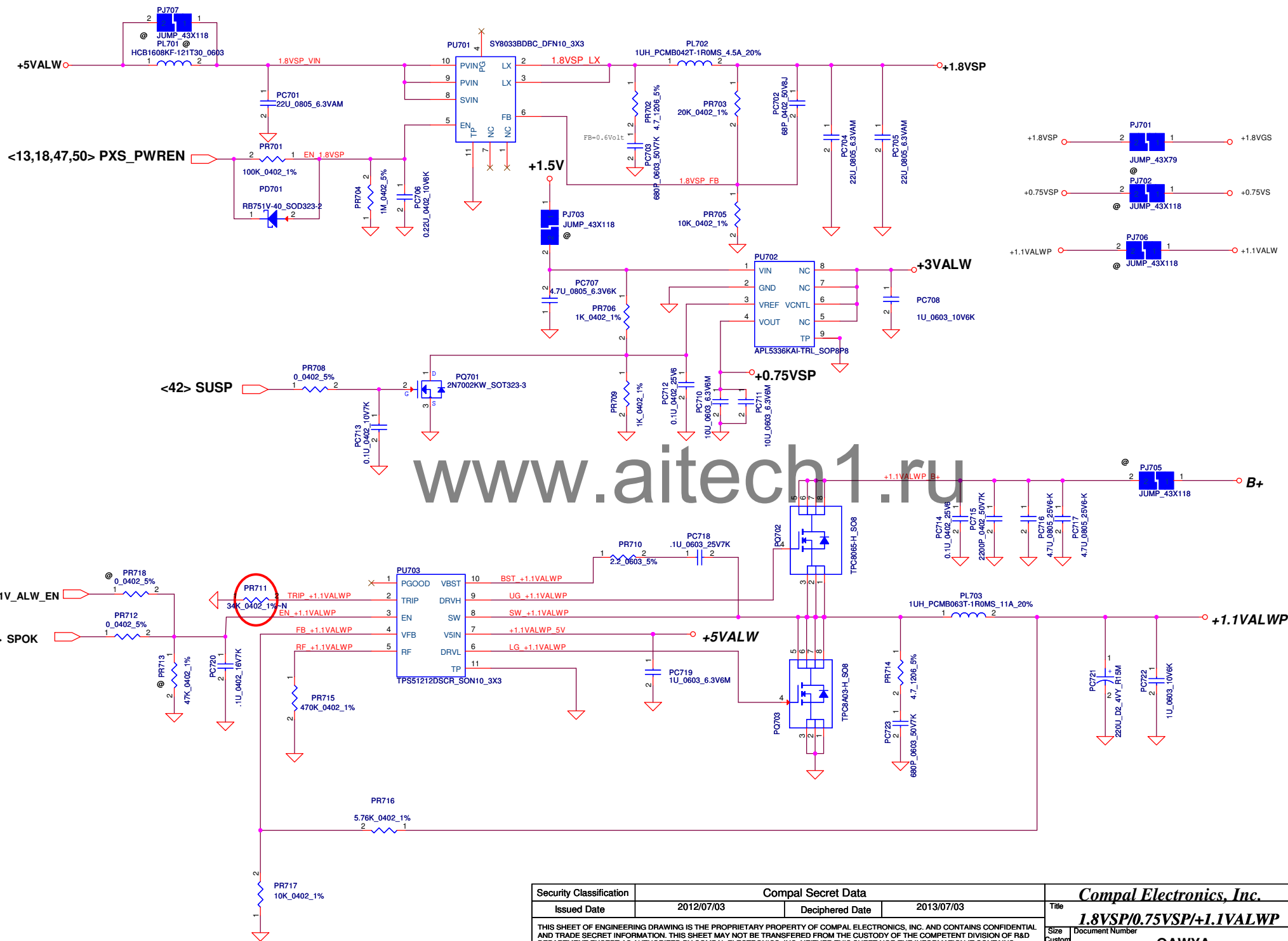
RT8205
TONSEL=VREF (1) SMPS1=300KHZ (+5VALWP)
(2) SMPS2=375KHZ (+3VALWP)
TPS51125A
TONSEL=VREF (1) SMPS1=245KHZ (+5VALWP)
(2) SMPS2=305KHZ (+3VALWP)

RT8205
+5VALWP I_{max}=11.1A ; I_{peak}=13.32A
1/2 Delta I=1.33A (F=300K Hz)
V_{trip}=0.0887V
R_{ds(on)}=7.0m ohm(max) ; R_{ds(on)}=5.1m ohm(typical)
I_{limit_min}=0.0887/7m=12.67A
I_{limit_max}=0.0887/5.1m=17.39A
I_{ocp}=I_{limit}+1/2Delta I=14A ~ 18.72A

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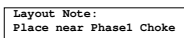
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|---------------|-----------------|-----------------------------------|--|
| Title | | Compal Electronics, Inc. | |
| 3VALWP/5VALWP | | Rev 0.1 | |
| Size | Document Number | QAWYA | |
| Custom | | Date: Thursday, November 01, 2012 | |
| Sheet 46 | | of 57 | |

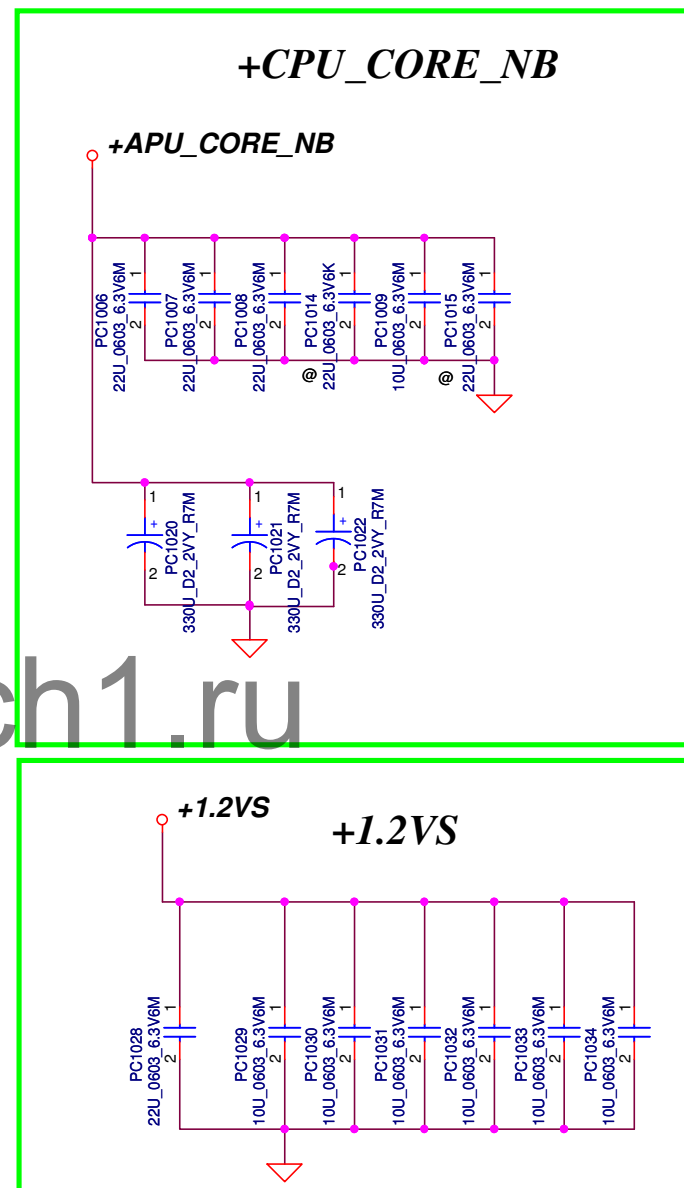
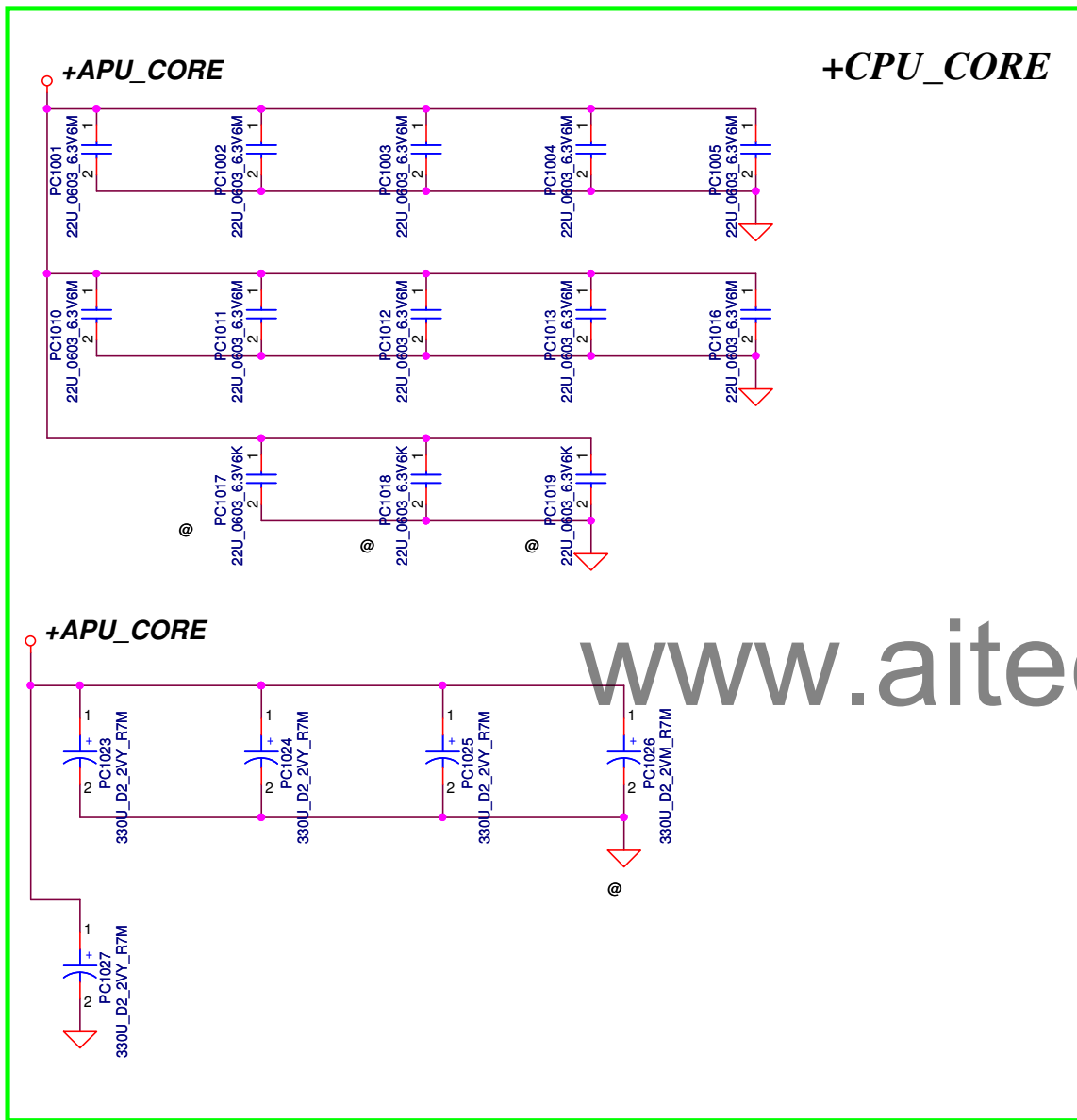
$$I_{ocp}=13.58A\sim23.10A$$




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|---|------------|--------------------|------------|--------------------------|-----------------------------|
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| Issued Date | 2012/07/03 | Deciphered Date | 2013/07/03 | Title | 1.8VSP/0.75VSP/+1.1VALWP |
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| | | | | Document Number | QAWYA |
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Default



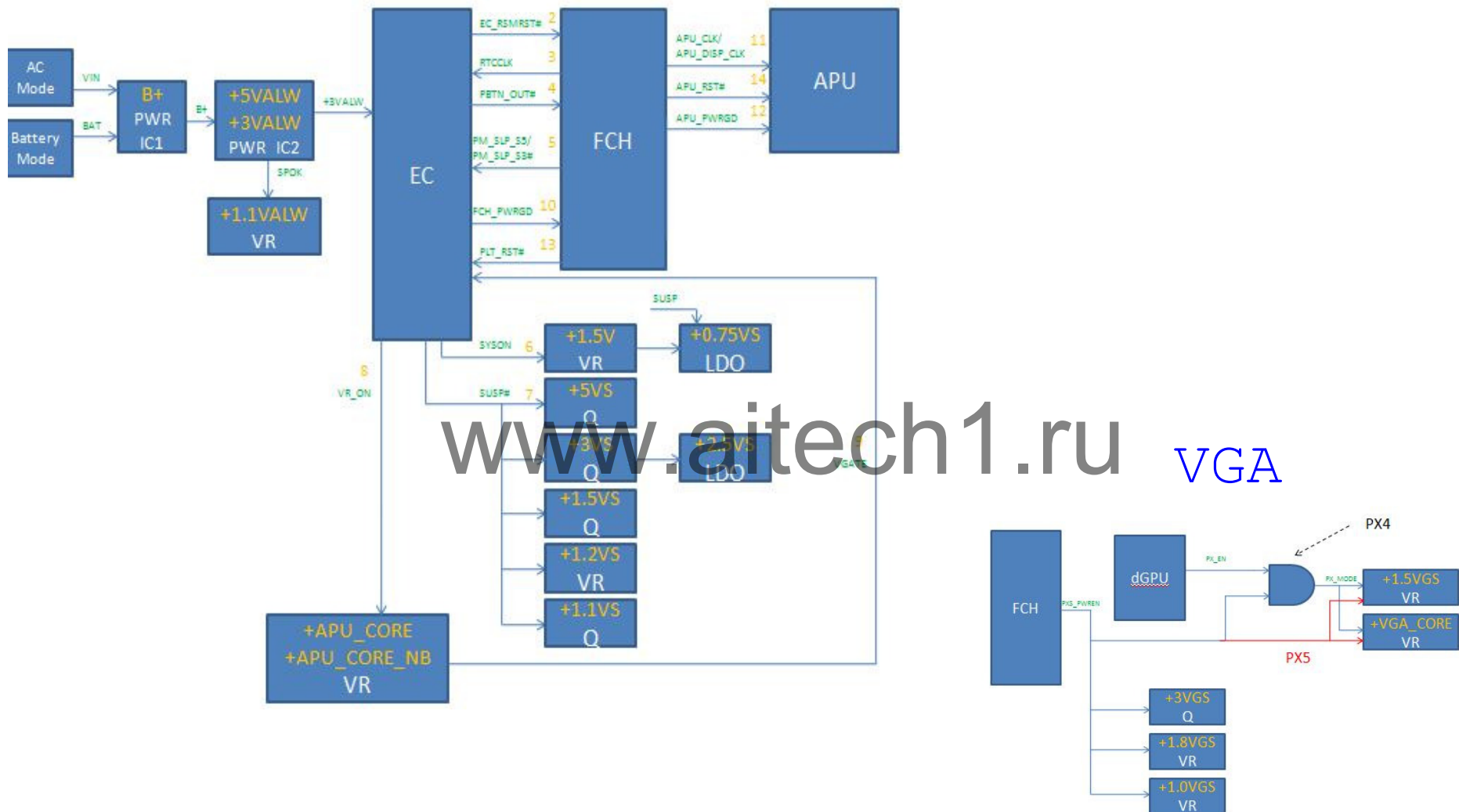


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| Item | Reason for change | PG# | Modify List | Date | Phase |
|------|---|-----|---|-------|-------|
| 1 | Change net name | | 1.FBVDDQ_PWR_EN to PX_MODE 2.VSSSENSE_VGA to VSSSENSE_VGA to PX_MODE 3.VCCSENSE_VGA to NVDD_PWR_EN to PX_MODE 4.NVDD_PWR_EN to PX_MODE 5.+1.5VS_VGA to +1.5VGS 6.+1.8VS to +1.8VGS 7.APU_VDDNB_SEN to APU_VDDNB_SEN_H VGA_CORE_SEN | 10/11 | |
| 2 | Doesn't need VDDQ_SENSE, 0.75VR_EN#, DPRSLPVR_VGA, 1.5V_VGA_PWROK | | 1.remove pr517 pc519 and net VDDQ_SENSE 2.remove pr707 and net 0.75VR_EN# 3.remove pr815 pr838 4.remove net 1.5V_VGA_PWROK | 10/11 | |
| 3 | Change net name | | 1.H_PROCHOT#_EC to H_PROCHOT# | 10/13 | |
| 4 | Change & add power component | | 1.add pc947 2.pc901 pc941 remove BOM structure 3.pc913 pc942 change value 50v to 25v 4.pr913 change 0603 to 0402 | 10/14 | |
| 5 | Change net name | | 1.change 1.8VGS_EN PIN SUSP# to PXS_PWREN | 10/14 | |
| 6 | Add power component & Net | | 1.add PR965 2.add net APU_IMON | 10/18 | |
| 7 | Change power component (For EMI) | | 1.change PR602,PR710 to 2.2 ohm 2.PR605 PC610 remove BOM structure 3.PR714 PC723 remove BOM structure | 10/19 | |
| 8 | Remove & add & change power component | | 1.add PR610 for VR_ON 2.remove PC811, PC812, PC813, PC814, PC815, PC816, PC817, PC818, PC820, PC822, PC825, PC826, PC827, PC828, PC829, PC830, PC831, PC835, PC839, PC840, PC841, PC842, PC843, PC844, PC845, PC846 3.Remove PR327 and net BM# 4.change PC819, PC821 to 22U PC832, PC833, PC834 to 10U | 10/21 | |
| 9 | Change power component | | 1.change PR306 to 200k 2.PC322 remove BOM structure 3.change PC947 to PL901 right side | 10/24 | |
| 10 | Change power component | | 1.PR502 remove BOM structure | 10/26 | |
| 11 | Change power component | | 1.change PL501, PL601, PL703 to 1UH_PCMB063T-1R0M5_11A 2.change PL901 _20% | 10/27 | |
| 12 | Add power component & Net | | 1.add PR718 and NET IV_ALW_EN 2.NET VR_ON change to VLDT_EN | 10/27 | |
| 13 | Change power component | | 1.move PR960, PR962 to near APU_VDD_SEN_H and APU_VDD_SEN_L | 10/28 | |
| 14 | update footprint | | 1.update PL901 footprint | 10/31 | |
| 15 | Change power component | | 1.change PR404 to 19.6k | 11/03 | |
| 16 | Change & Add power component | | 1.add PR966, PR967 2.change PC712 to 0.1U_0402_25V6 | 11/07 | |
| 17 | Change power component | | 1.change PQ316 to 2N7002KW_SOT323-3 | 11/09 | |
| 18 | Change power component | | 1.change PQ302 to A04423L | 11/11 | |
| 19 | Change page number | | change P44~P54 to P43~P53 | 11/15 | |
| 20 | Change power component | | 1.change PQ301, PQ303 to A04423L | 11/16 | |
| 21 | Change power component & Add BOM structure | | 1.PQ802 PQ805 PQ807 PQ808 add BOM structure 2.change PR701 to 100K, PC706 to 0.22U, PR524 to 120K 3.change PR522 to 169K, PR802 to 130K 4.PC515 remove BOM structure | 11/17 | |
| 22 | Change power component | | 1.change PC314 to SE042473M80 0.047U 25V M X7R 0603 | 11/18 | |
| 23 | Change power component | | 2.change PR524 to 40.2K | 11/21 | |
| 24 | Change & Add power component | | 1.change PC508 (SF000002N00) to (SF000002Y00) 2.add PU201, PR207, PR208, PR209, PR210, PR213, PC203, PQ201 remove PR206 | 11/22 | |
| 25 | Change power component | | 1.change PC832, PC833, PC834 to 0603 size | 11/30 | |
| 26 | Change power component | | 1.change PR824 to 56k 2.move PC947 to connect CPU_B+ 3.change PR837 to 1.1k 4.PR525 add BOM structure | 12/8 | |
| 27 | Add Net | | 1.Add VSB_ON | 12/9 | |
| 28 | Change power component | | 1.6277 pin30 connect to +5VALW | 12/12 | |
| 29 | Change power component | | 1.PL302 change to 10u (SH000005280) 2.PQ503, PQ504 change to power pak 5-6 3.PR228, PR229, PR230, PR231, PR232 | 12/13 | |
| 30 | Change power component | | 1.PR918, PR932 change to 133k PR924, PR943 change to 10.5k | 12/20 | |
| 31 | Change & Add power component | | 1.add PD701 for HW turn off time 2.change PR601 to 300k | 12/22 | |
| 32 | Change power component | | 1.change PJ301 to PL301 | 12/23 | |
| 33 | Change power component | | 1.change PL301 and PL702 to SH000001W00 | 12/26 | |
| 34 | Change & Add power component | | 1.add PR233 | 12/28 | |
| 35 | Change power component | | 1.change PR522 to 30k | 1/4 | |
| 36 | Change power component | | 1.change PR126, PR413, PR801, PR814, PR842, PR843, PR844, PR845, PR846, PR847 footprint | 2/16 | |

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SYSTEM



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|---|------------|--------------------|------------|--------------------------|-----------------------------|
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QAWYA HW PIR List

| NO | | DATE | PAGE | MODIFICATION LIST | | PURPOSE | EVT TO DVT | | NOTE |
|----|--|------|------|-------------------|--|---------|------------|--|------|
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QAWYA HW PIR List DVT->PVT

| NO | DATE | PAGE | MODIFICATION LIST | PURPOSE | NOTE |
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| | | | | Custom | LA8642P M/B | 0.1 |
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| NO | DATE | PAGE | MODIFICATION LIST | PURPOSE | NOTE |
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